

MathWorks
**AUTOMOTIVE
CONFERENCE 2023**
Europe

Software Architectures and Virtual Integration in Model-Based Design

Mani Ramamurthy, MathWorks



IEEE Spectrum This Car Runs on Code

FEATURE TRANSPORTATION

THIS CAR RUNS ON CODE

It takes dozens of microprocessors running 100 million lines of code to get a premium car out of the driveway, and this software is getting more complex

2009

BY ROBERT N. CHARETTE | 01 FEB 2009 | 7 MIN READ



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Software isn't just running our vehicles. It's defining them

Taylor Armerding · Follow

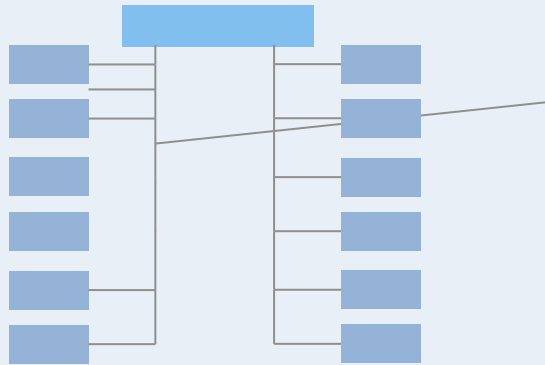
Published in Nerd For Tech · 6 min read · Jun 26

2023

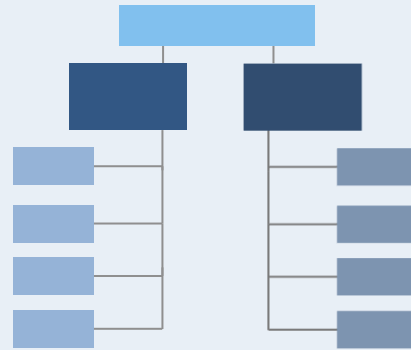


E/E architectures

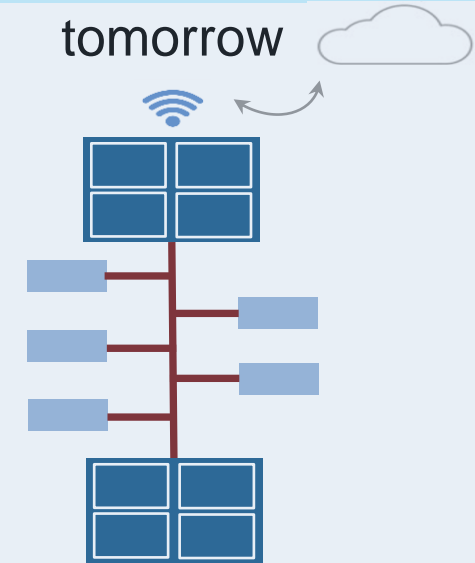
yesterday



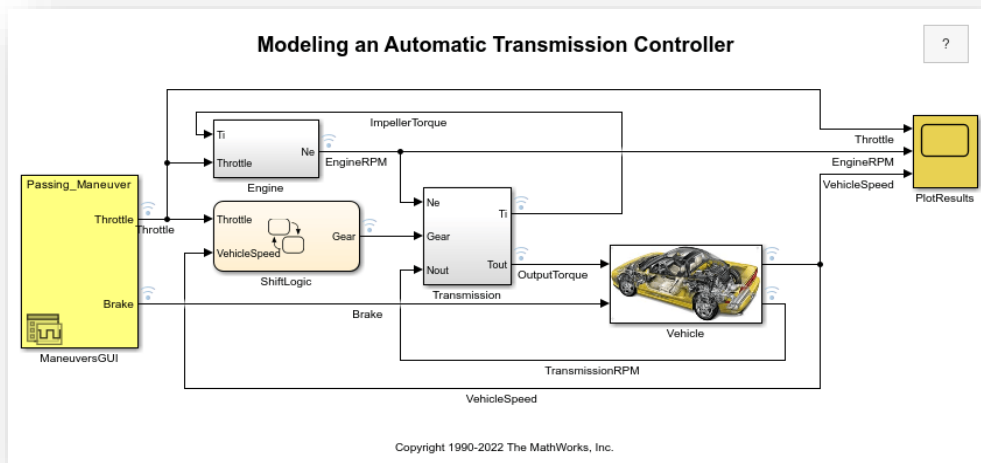
today



tomorrow



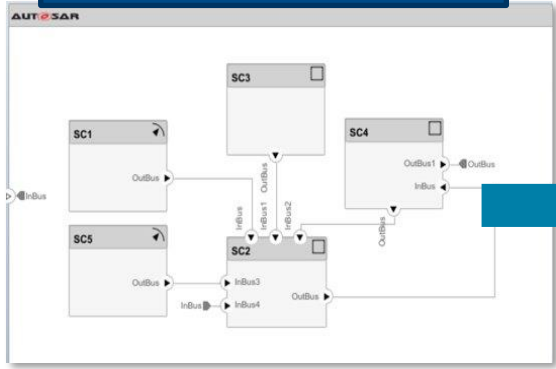
Modeling & Design Tools



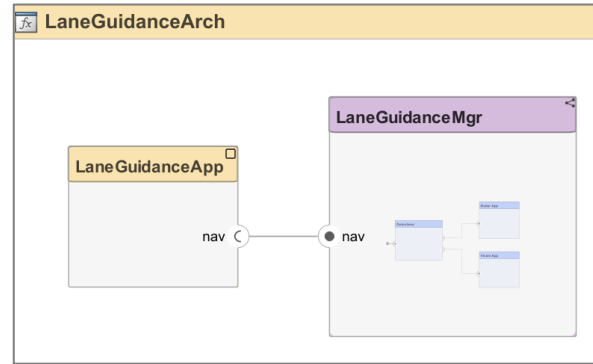
How is the Simulink Platform staying relevant?

Models → Architectures

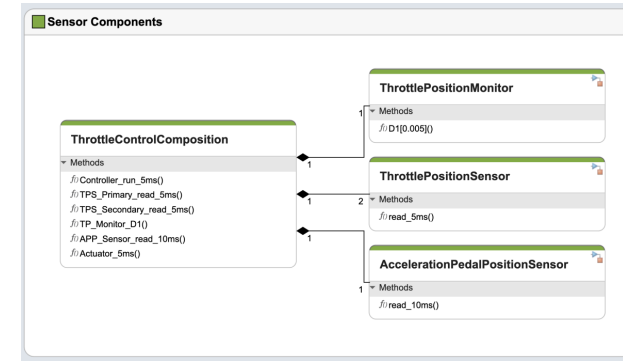
ARCHITECTURE



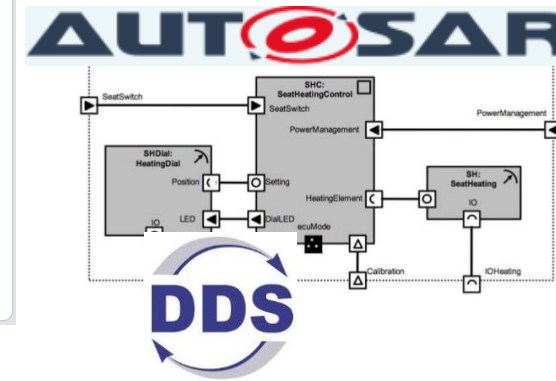
SOA



OO PRINCIPLES



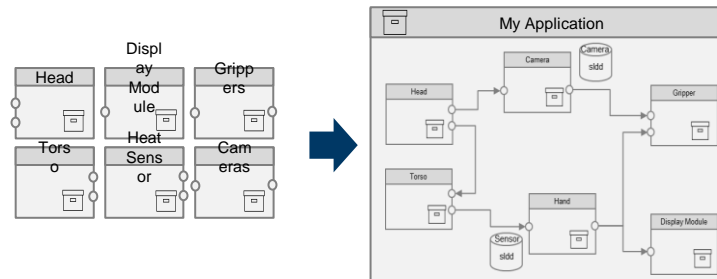
FRAMEWORKS



Simulation → Virtual Integration



AUTO ASSEMBLY



CODE CENTRIC DESIGN

```
class CodeComponent final
{
public:
// service function f0
void f0(real_T rtu_u, real_T
*rt_y);
// service function f1
```

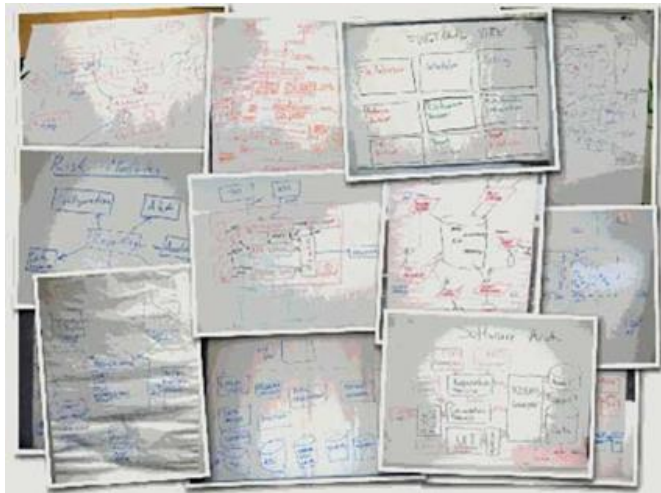
CI



From Models to Architectures

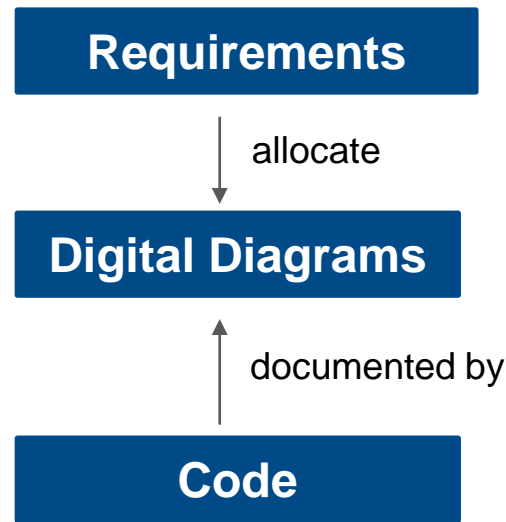
Software architectures are abstractions to get good implementations

Facilitate the creative design process

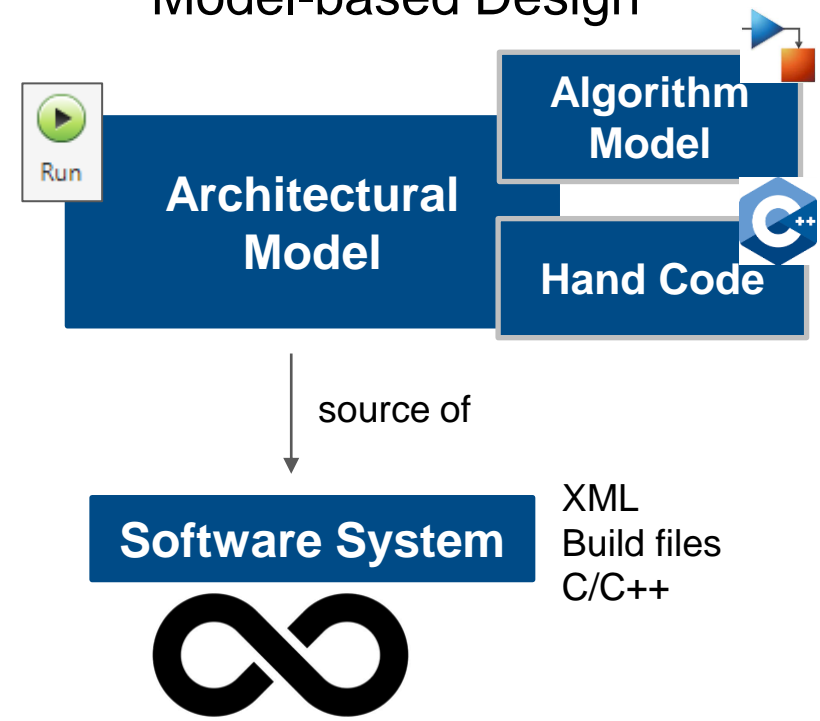


Low barrier of entry

Conform to digital engineering

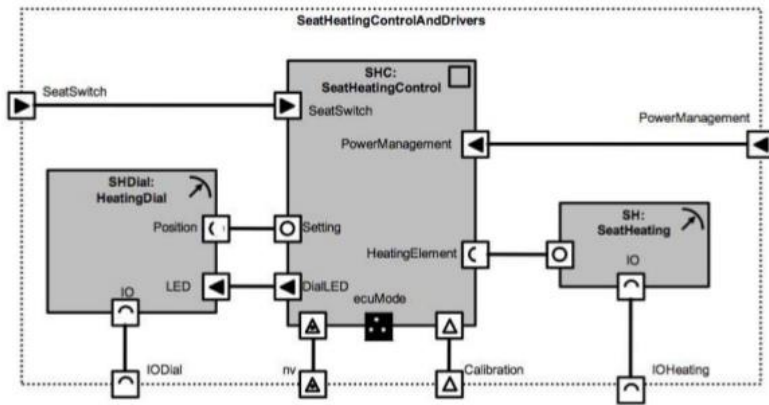


Implement leveraging Model-based Design

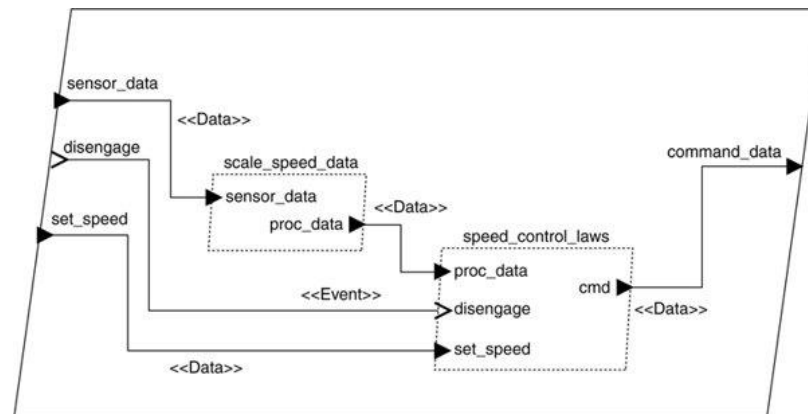
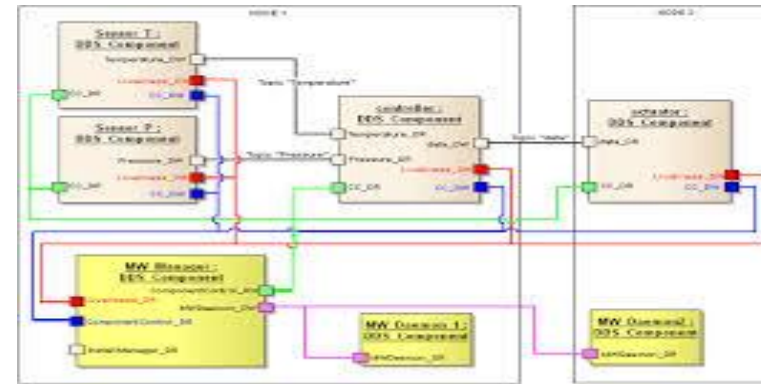


Completes deep workflow

Component-Port-Connector Diagrams are standard representations of Software Architectures



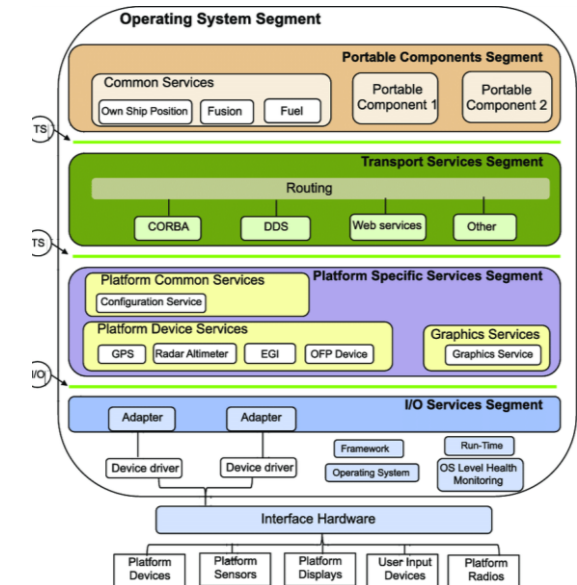
AUTOSAR



AADL

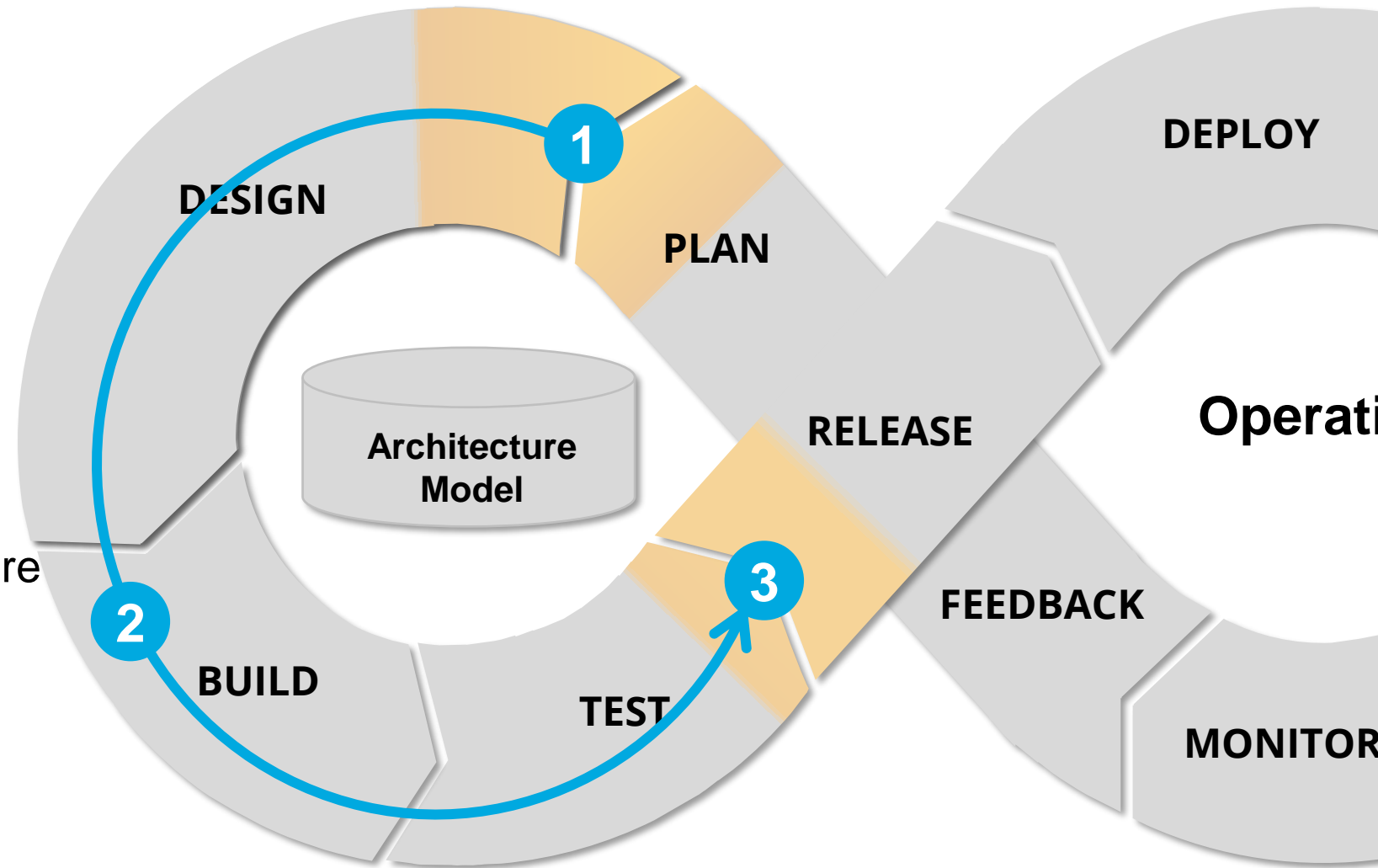


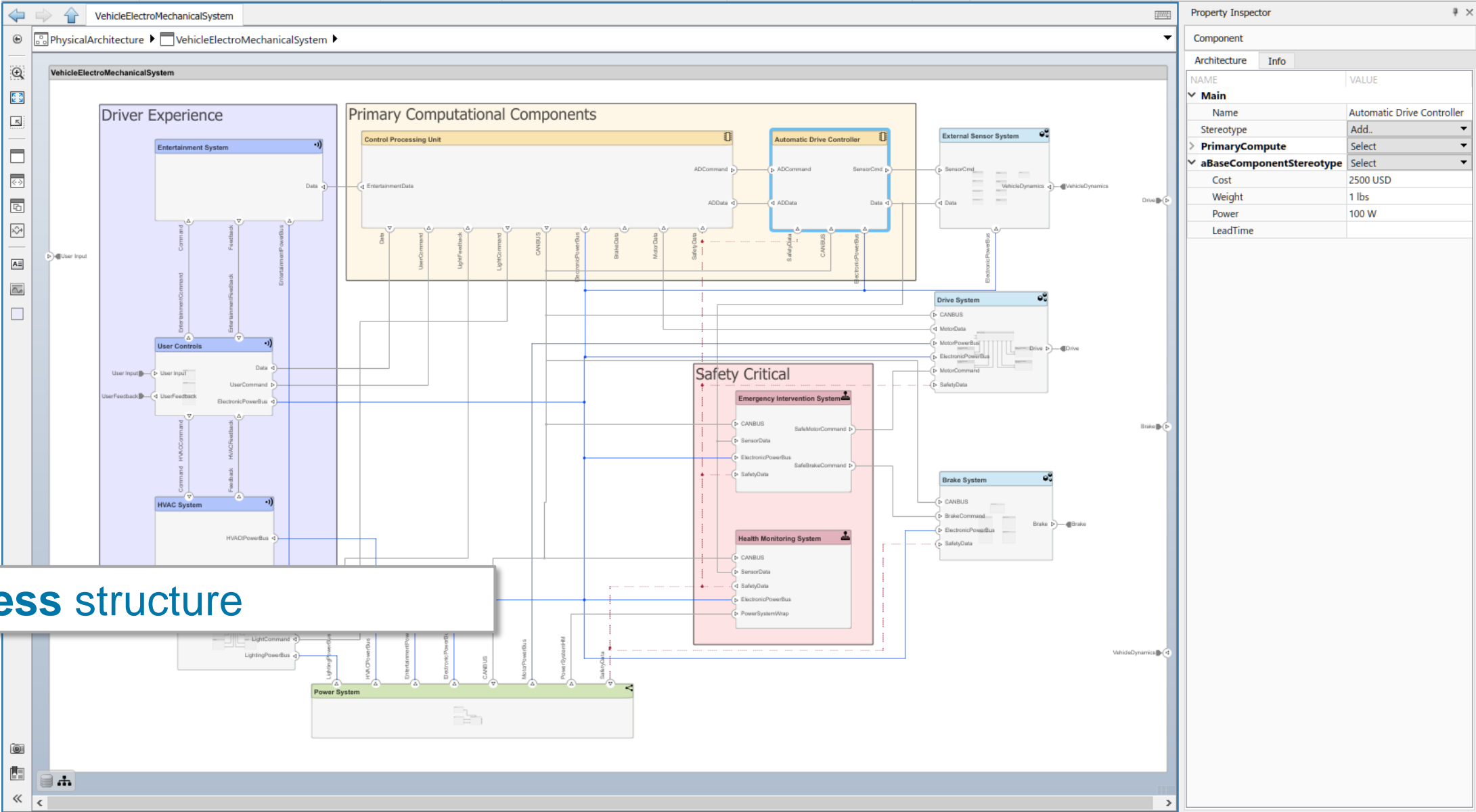
Future Airborne Capability Environment



System Composer is our platform for Architecture Modeling

- 1** Expressive & intuitive architecture modeling language
- 2** Seamless and Collaborative workflow between MBSE, Software Architecture and MBD
- 3** Rigor for virtual system integration, test, and implementation

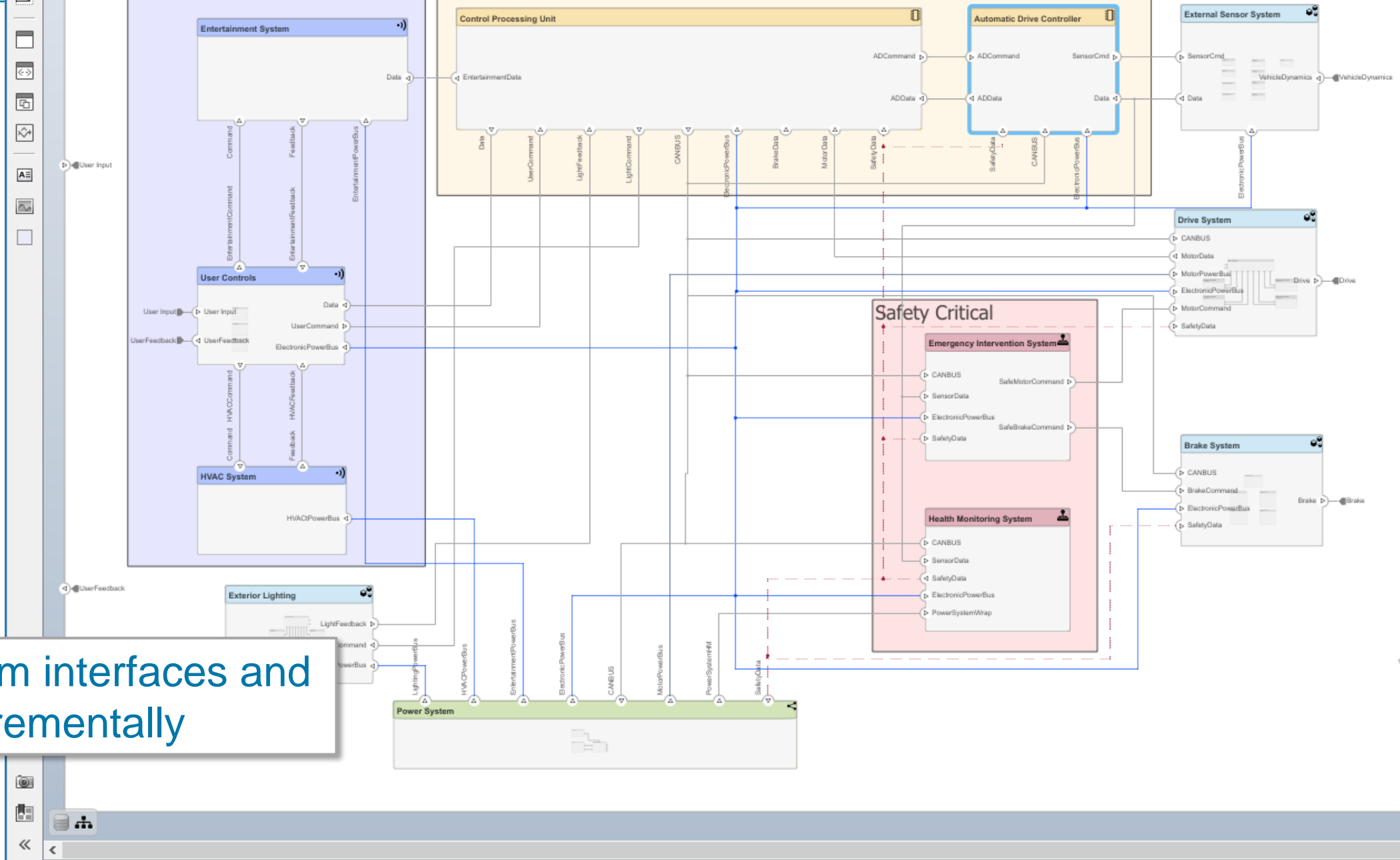




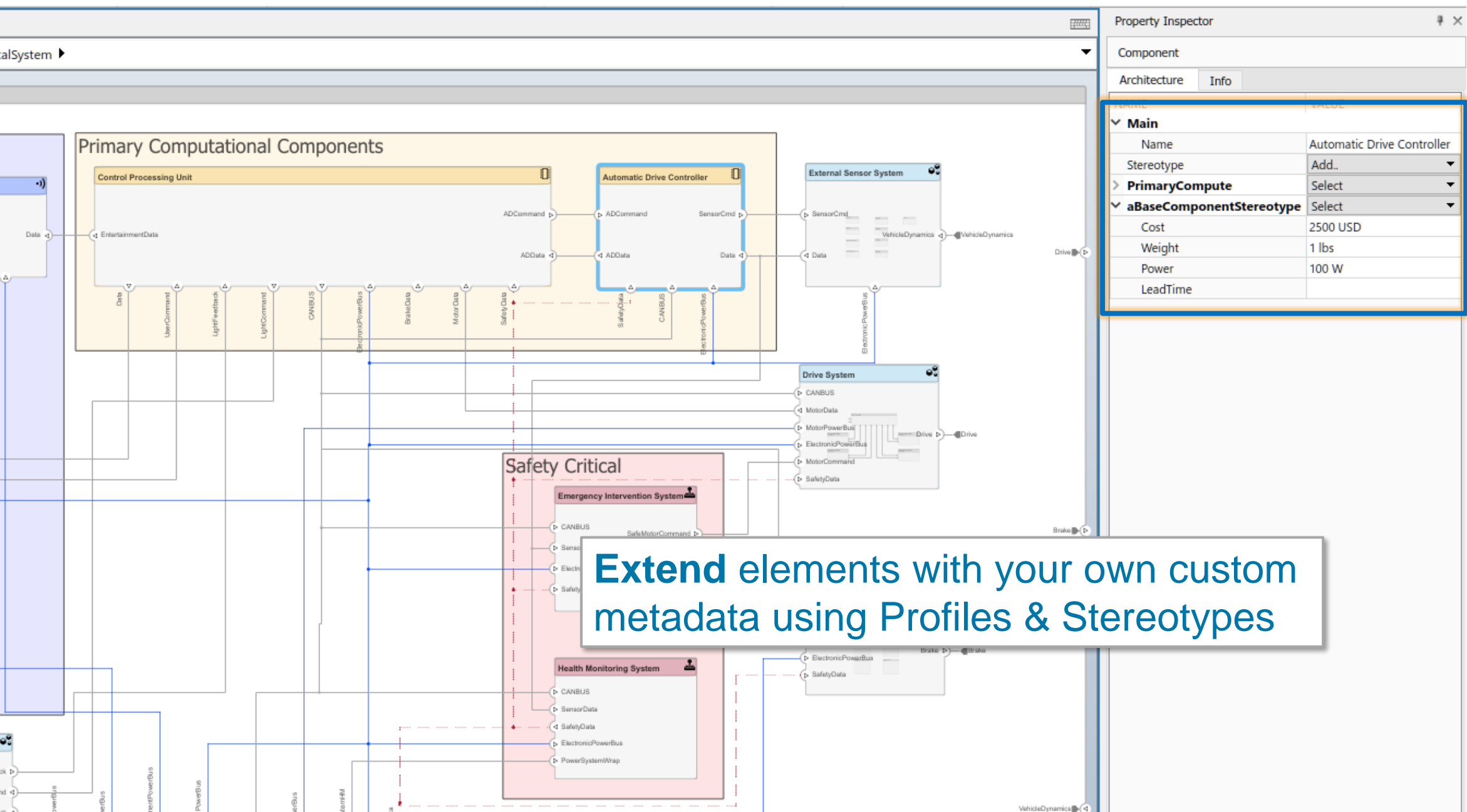
Express structure

Driver Experience

Primary Computational Components



Sketch system interfaces and elaborate incrementally



Property Inspector

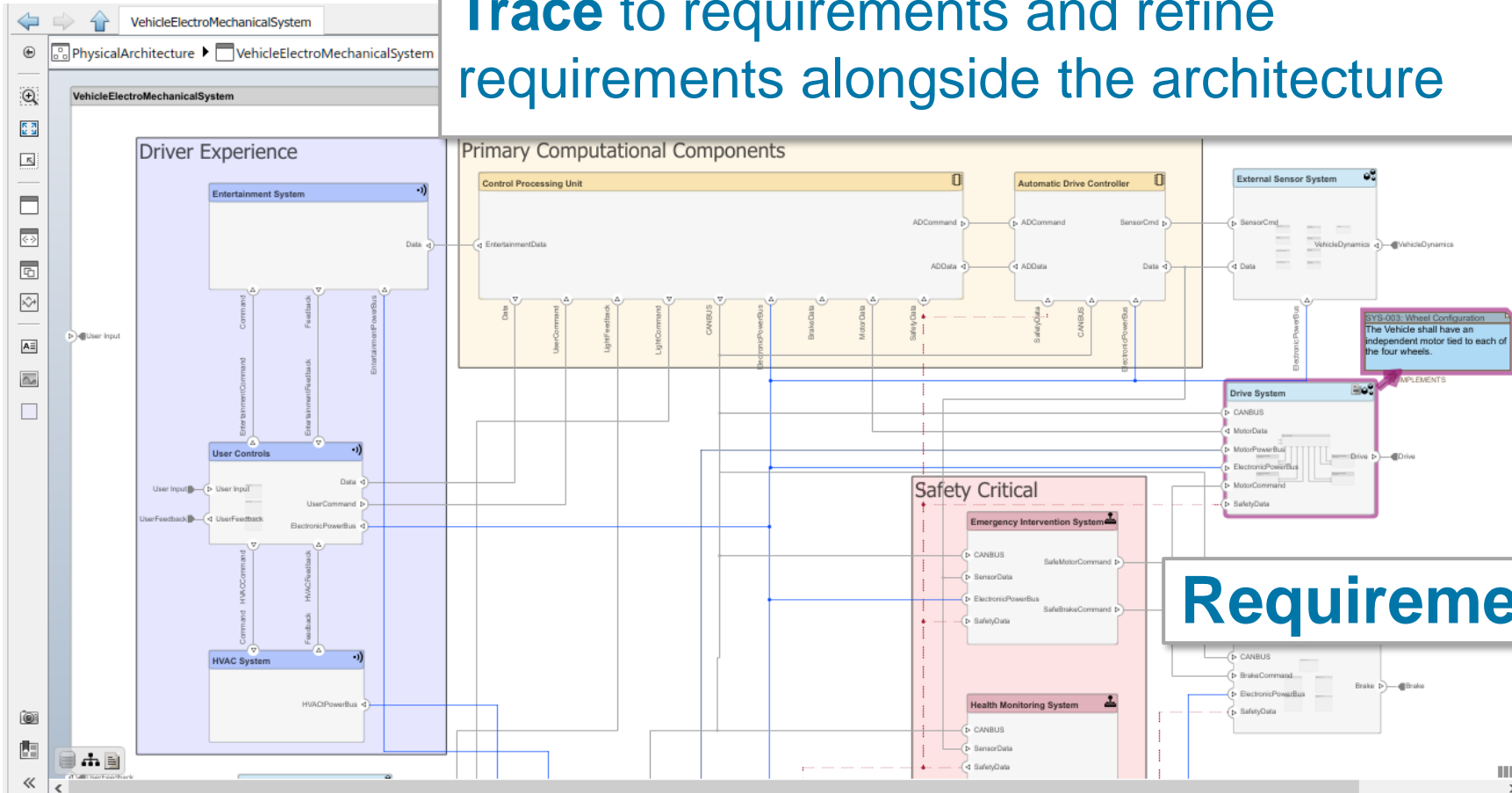
Component

Architecture Info

Main	
Name	Automatic Drive Controller
Stereotype	Add..
PrimaryCompute	Select
aBaseComponentStereotype	Select
Cost	2500 USD
Weight	1 lbs
Power	100 W
LeadTime	

Extend elements with your own custom metadata using Profiles & Stereotypes

Trace to requirements and refine requirements alongside the architecture



Property Inspector

Requirement: SYS-003

Details

▼ Properties

Type: Functional

Index: 3.3

Custom ID: SYS-003

Summary: Wheel Configuration

Description Rationale

The Vehicle shall have an independent motor tied to each of the four wheels.

Keywords:

Revision information:

▼ Custom Attributes

▼ Links

Implemented by:

[Drive System](#)

▼ Comments

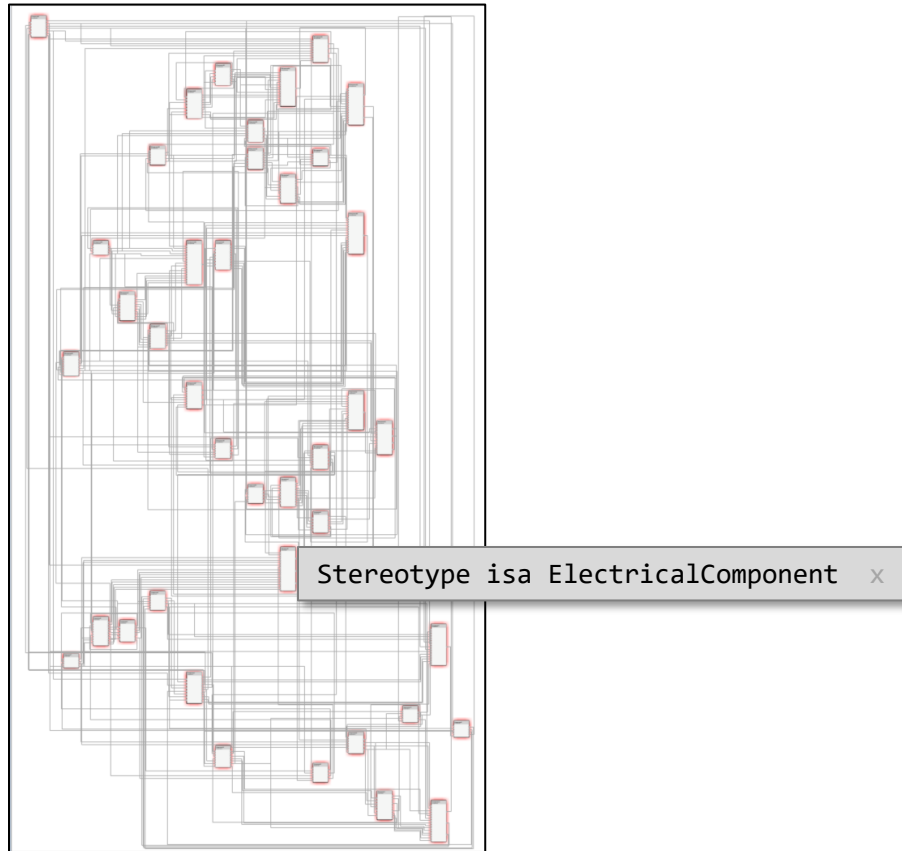
Requirements Toolbox™

Requirements - PhysicalArchitecture

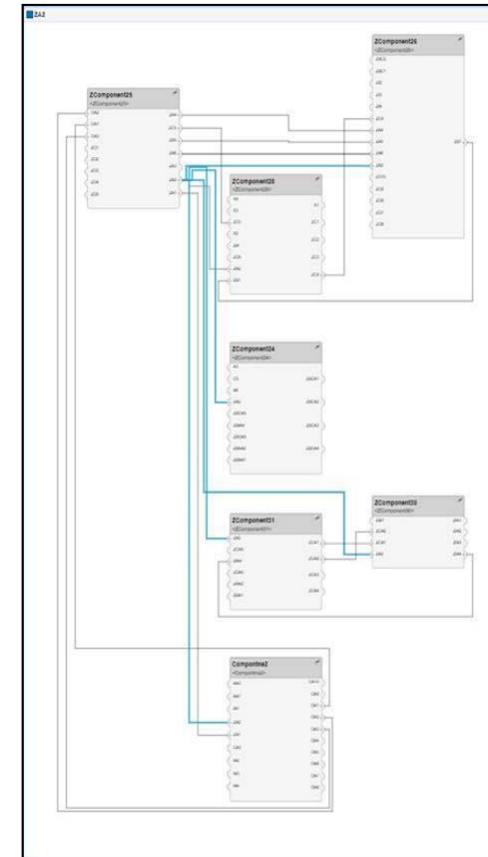
View: Requirements

Index	ID	Summary	Implemented
> 2	Logical	Logical Requirements	<div style="width: 100%; height: 10px; background-color: blue;"></div>
▼ 3	Physical	Physical Requirements	<div style="width: 100%; height: 10px; background-color: blue;"></div>
3.1	SYS-002	Range	<div style="width: 100%; height: 10px; background-color: blue;"></div>
3.2	SYS-001	Power Source	<div style="width: 100%; height: 10px; background-color: blue;"></div>
3.3	SYS-003	Wheel Configuration	<div style="width: 100%; height: 10px; background-color: blue;"></div>
3.4	Environmental	Environmental Requirements	<div style="width: 100%; height: 10px; background-color: blue;"></div>

Simplify the complex with Filters and autogenerated Views



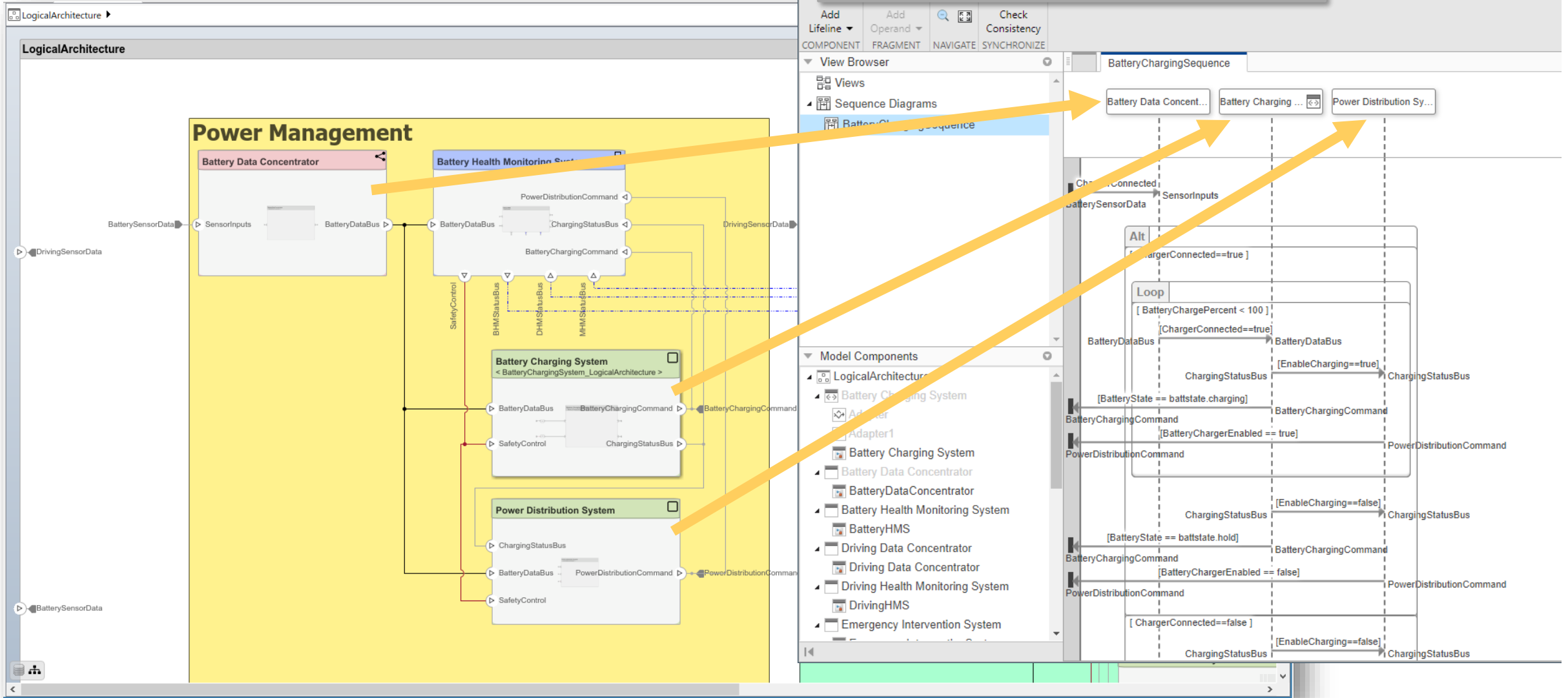
Full system model



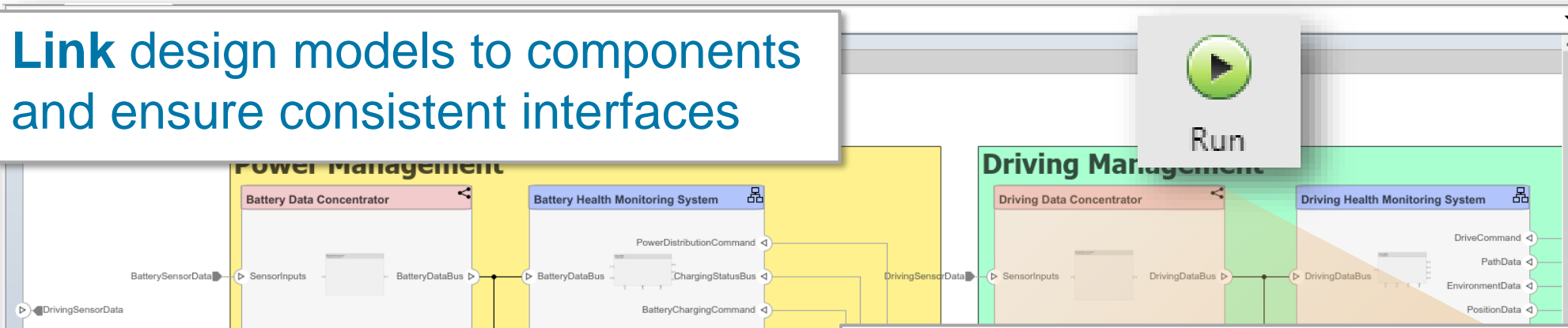
Filtered view

Define behaviors and keep them synchronized with your architecture

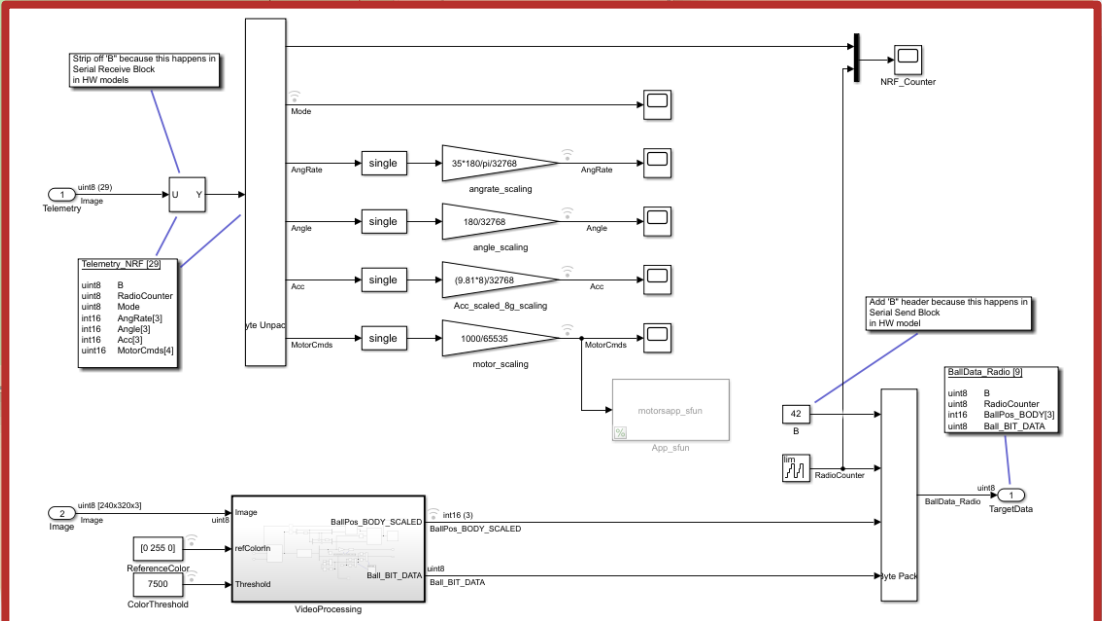
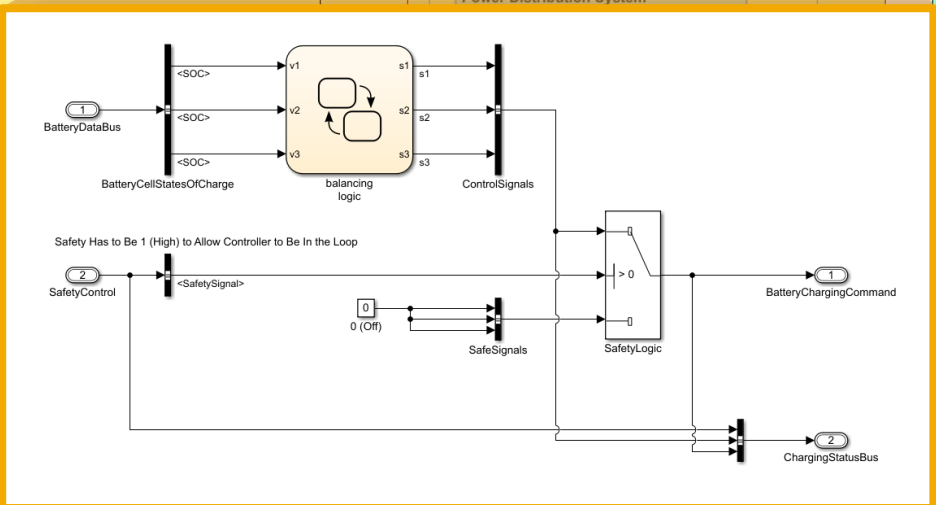
Sequence Diagrams



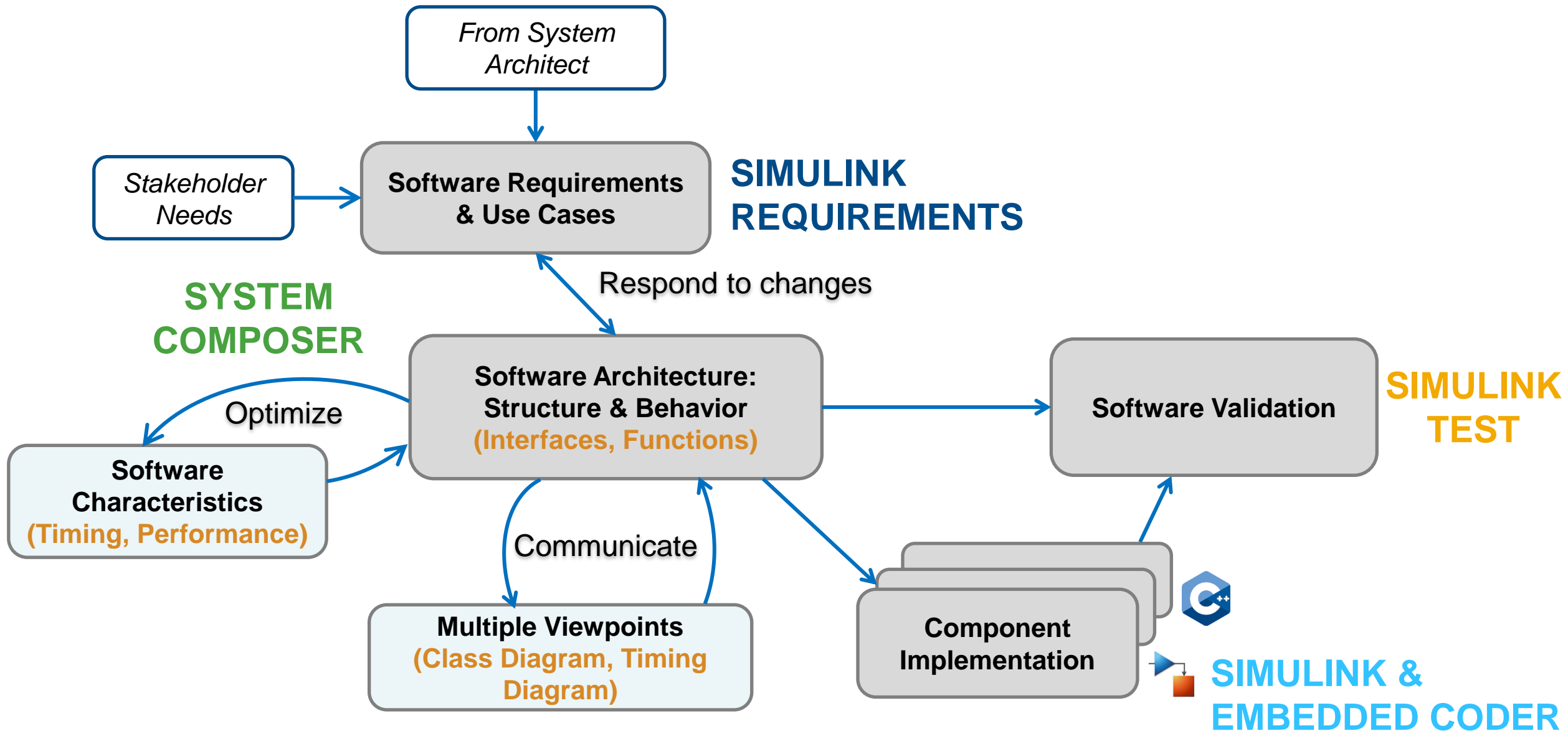
Link design models to components and ensure consistent interfaces



Simulink® and Model-Based Design



User Workflow for Software Architecture Modeling



Represent Internal Behavior of Components as Functions

Throttle Position Control System

The diagram shows the following components and their connections:

- TPS_Sensor** (Accelerator/ThrottlePositionSensor) provides `APP_HwID_Value` to **TPS_Sensor** and **APP_Sensor**.
- TPS_Sensor** provides `TPS_HwID_Value` to **TPS_Sensor** and **TP_Monitor**.
- TPS_Sensor** provides `TPS_Percent_Value` to **TPS_Sensor** and **TP_Monitor**.
- TP_Monitor** provides `TPS_Sensor_Value` and `TPS_Primary_Value` to **TP_Monitor**.
- TP_Monitor** provides `TPM_Percent_Value` to **Controller**.
- APP_Sensor** provides `APP_HwID_Value` and `APP_Percent_Value` to **Controller**.
- Controller** provides `ThrCmd_Percent_Value` and `ThrCmd_HwID_Value` to **Actuator**.
- Actuator** provides `ThrCmd_HwID_Value` to **Actuator**.

Functions Editor

Execution Order	Function Name	Software Component	Period
1	f() Actuator_output_5ms	Actuator	-1
2	f() Controller_run_5ms	Controller	0.005
3	f() TPS_Primary_read_5ms	TPS_Primary	0.005
4	f() TPS_Secndary_read_5ms	TPS_Secndary	0.005
5	f() TP_Monitor_D1	TP_Monitor	0.005
6	f() APP_Sensor_read_10ms	APP_Sensor	0.01

ThrottleControlComposition: Schedule Editor

The diagram shows the following execution order:

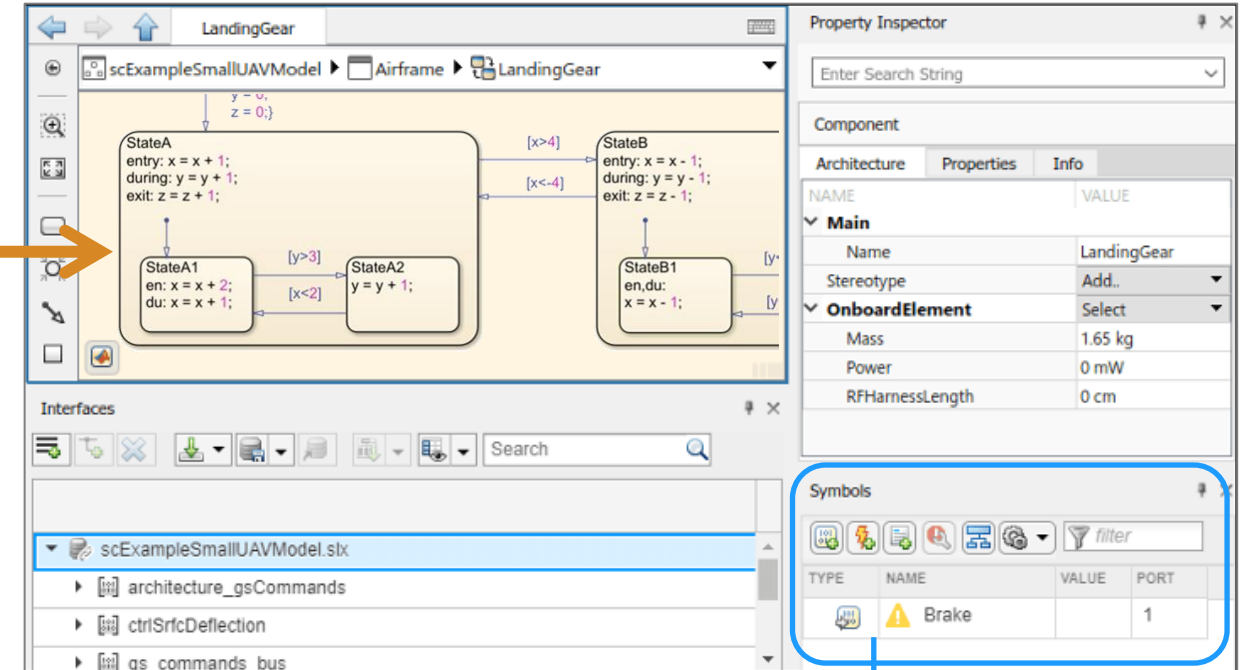
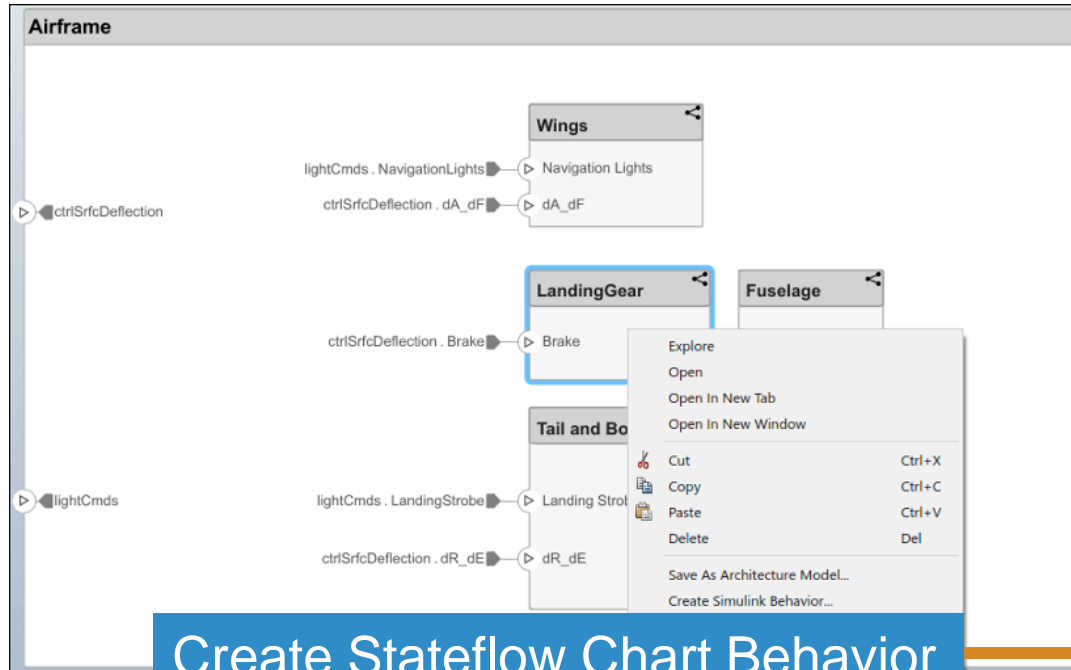
- 0.005 TPS_Secndary_read_5ms
- 0.005 TPS_Primary_read_5ms
- 0.005 TP_Monitor_D1
- 0.005 Controller_run_5ms
- 0.01 APP_Sensor_read_10ms
- A Actuator_output_5ms

Order

Order	Name	Trigger
1	Actuator_output_5ms	A
2	Controller_run_5ms	
3	TPS_Primary_read_5ms	
4	TPS_Secndary_read_5ms	
5	TP_Monitor_D1	
6	APP_Sensor_read_10ms	

Define behaviors and keep them synchronized with your architecture

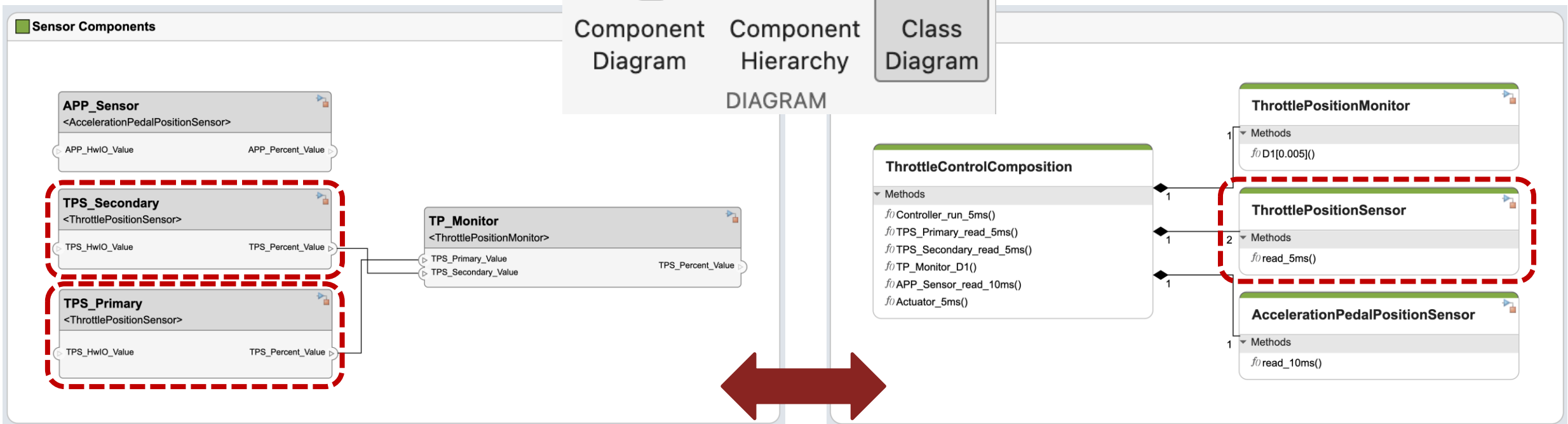
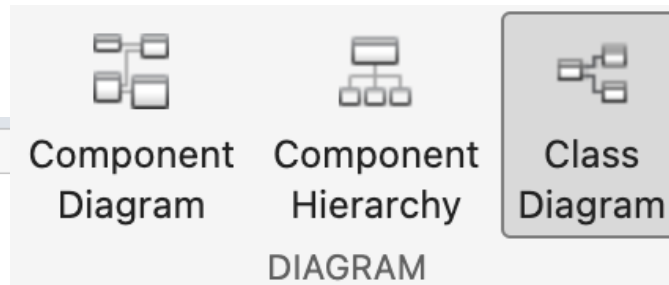
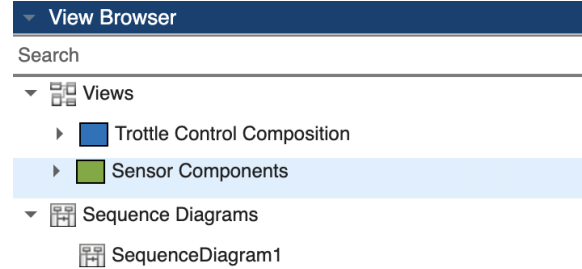
State Charts



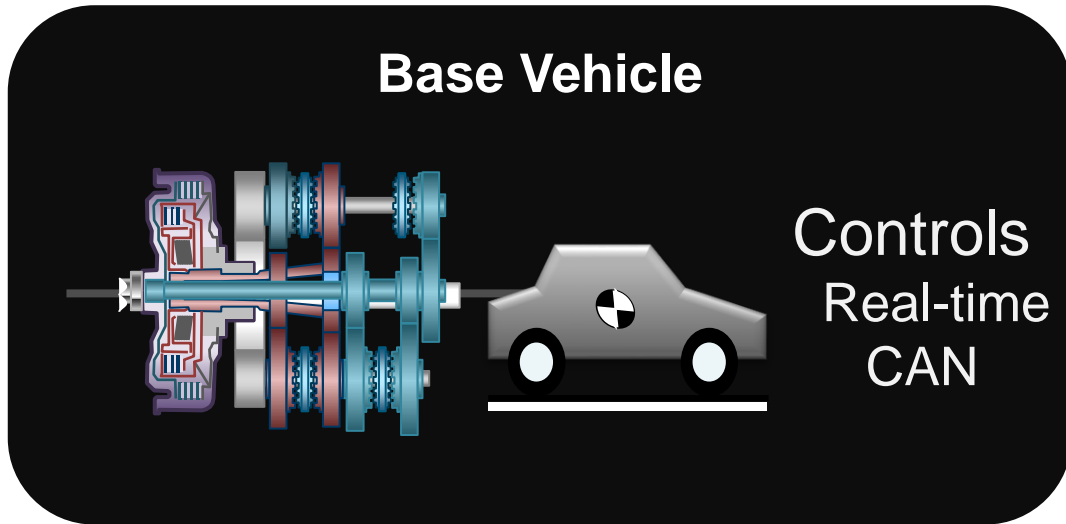
State chart with pre-configured interface

Class Diagram shows unique types of components

“How many throttle position sensors do I have in my software?”



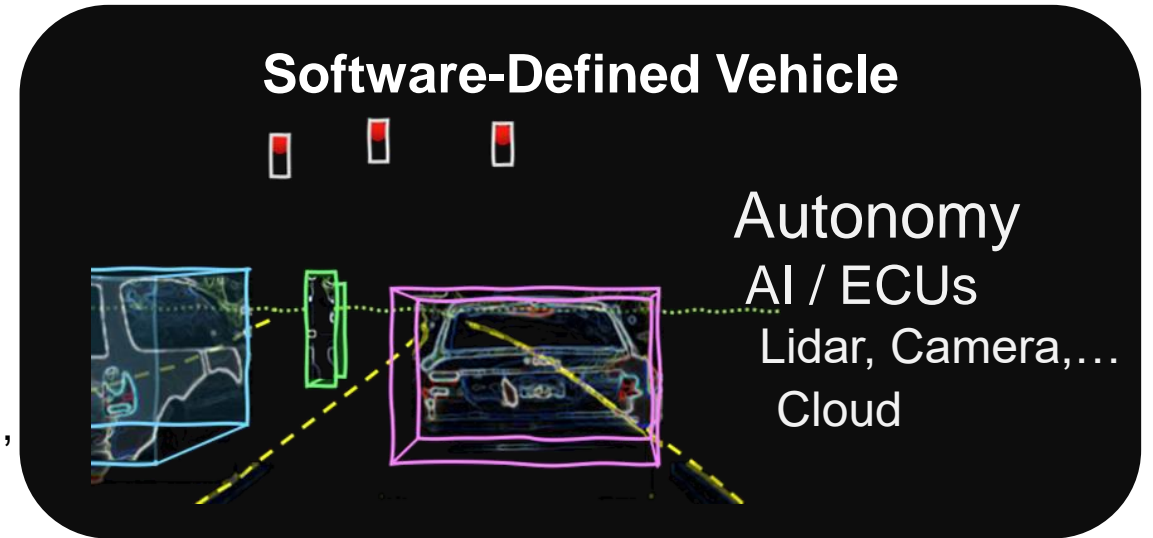
Trending to Service-Oriented Architecture (SOA)



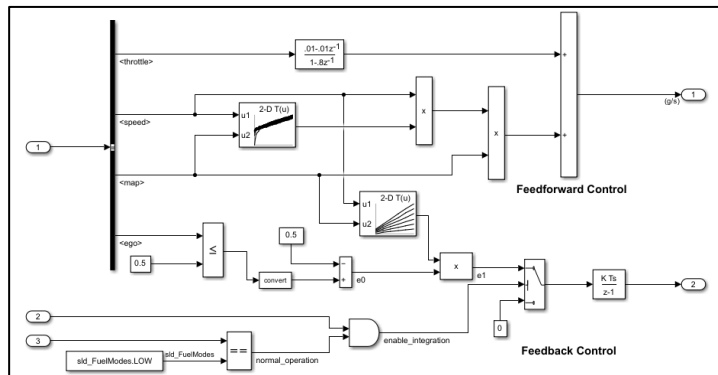
Speed,
Velocity



Steering,
Braking



Service-Oriented Architectures

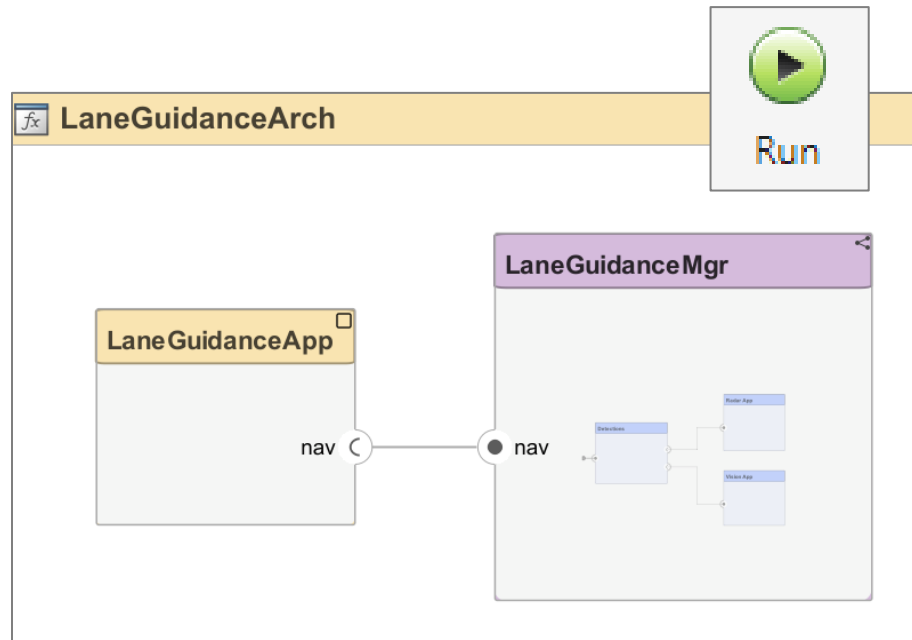


Model-Based Design

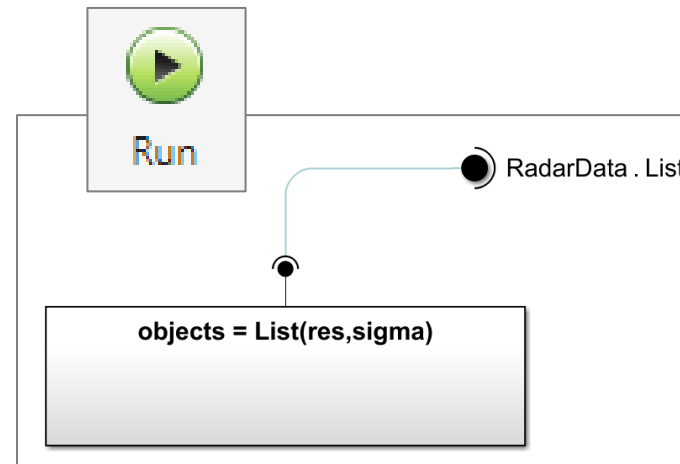


Microservice + service interface

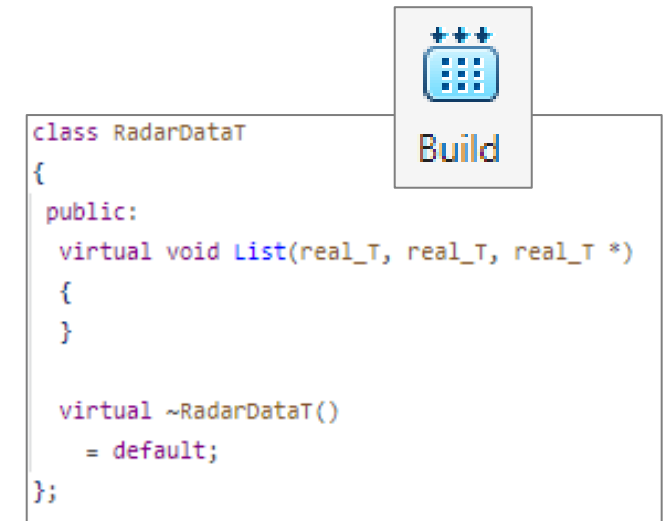
Service-Oriented Architecture (SOA) Design



Describe SOA with
System Composer

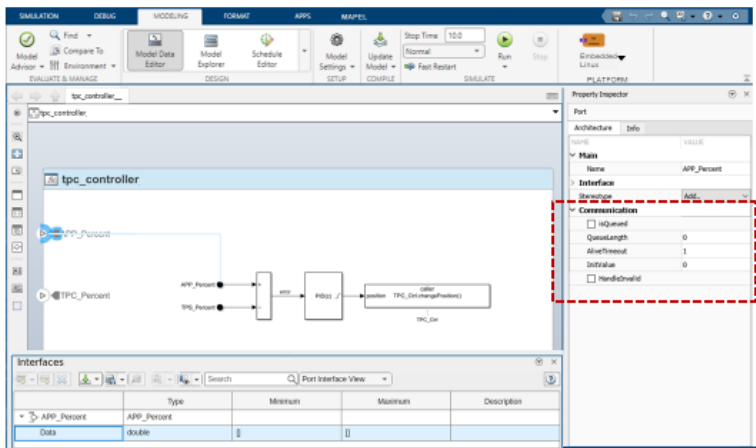


Implement detailed
components with
Simulink



Generate code with
Embedded Coder

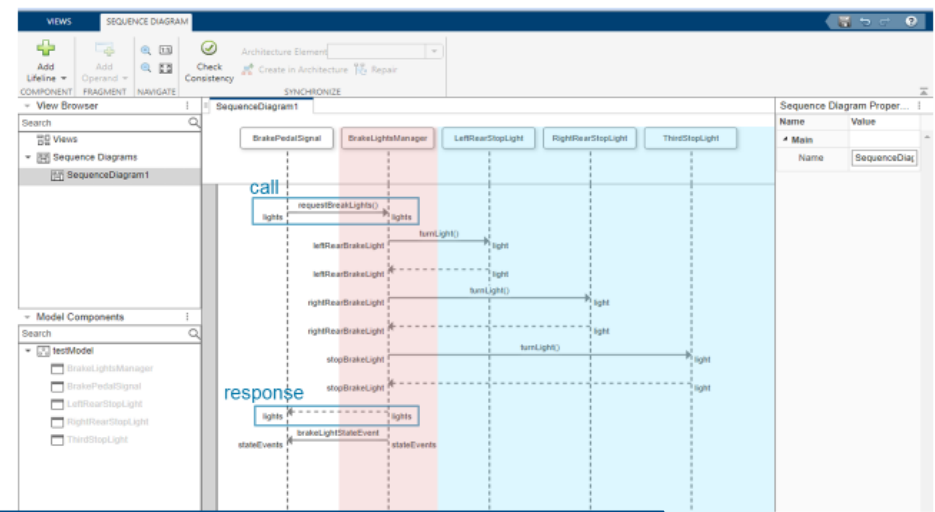
Specify and Simulate QoS Communication Attributes that Exist for All Platforms



- Port elements have universal attributes that exist for all platforms
 - Queue properties
 - Error status
- Attributes extensible with stereotypes

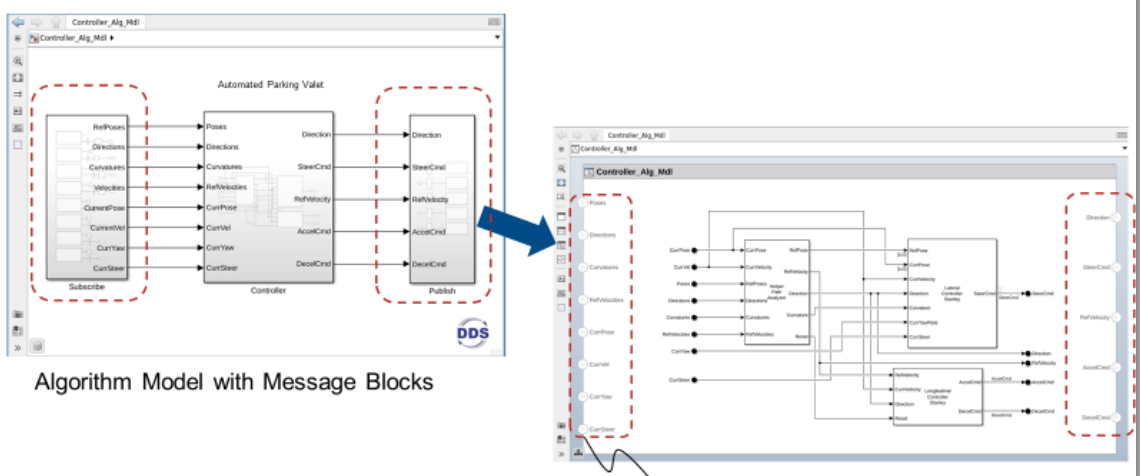
Interface Editor

Sequence Diagram for Client-Server



COMPLETE SOA WORKFLOWS

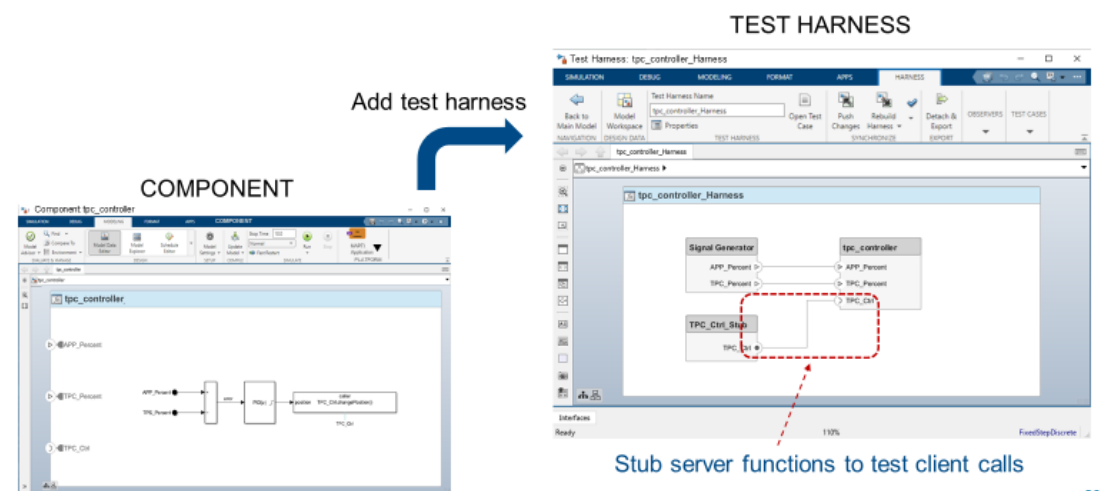
Adapt Algorithmic Models to Architecture Interfaces



Algorithm Model with Message Blocks

Component Perspective

Test Harness Workflow Creates Software Compositions to Simulate a Component

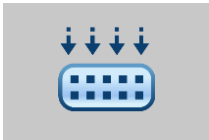
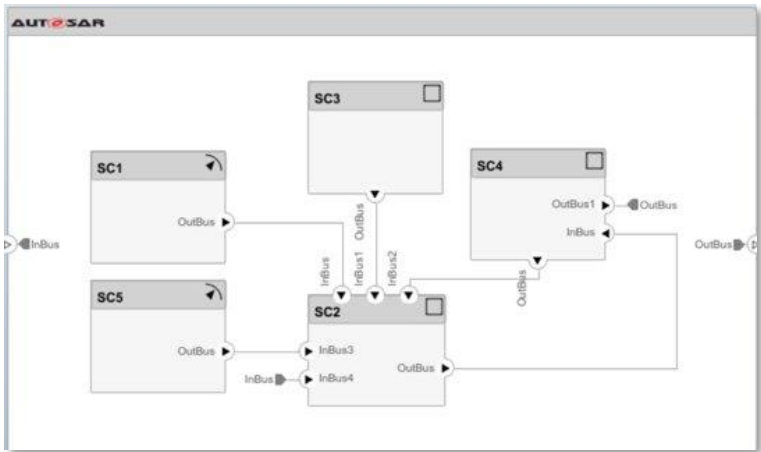


COMPONENT

TEST HARNESS

Stub server functions to test client calls

AUTOSAR Architecture



R2019b

- Strong support for Classic
- Growing support for Adaptive

Software Architecture

The screenshot shows the System Composer interface with a file list on the left and a central workspace. The workspace displays two models: 'Architecture Model' and 'Software Architecture Model'. A 'Create Model' button is visible in the 'Software Architecture Model' view. A sidebar on the right provides information about 'Software Architecture' by The MathWorks, Inc., including a brief description: 'Create a software architecture, ports, and connectors. Use components.'

R2021a

- Embedded coder support package for Linux emerging

DDS Application

The screenshot shows the Simulink Library Browser window. The search bar contains the text 'limit'. The 'DDS Blockset' is selected in the left-hand pane. The right-hand pane displays two blocks: 'Take DDS Sample' and 'Write DDS Sample', each represented by an envelope icon.

R2021a

untitled

untitled

untitled

Property Inspector

Architecture

Architecture Info

NAME	VALUE
<ul style="list-style-type: none"> <ul style="list-style-type: none"> Name Stereotype Parameters 	<ul style="list-style-type: none"> untitled Add.. Select
No parameters defined	

Interfaces

Search Dictionary View

	Type	Dimensions	Units	Complexity	Minimum	Maximum	Description	Asynchronous
untitled.slx								

Towards Virtual Integration and Simulation

Shifting Left: How far can you go?

Verify that the integration of Application SW components into full Application meets functional requirements

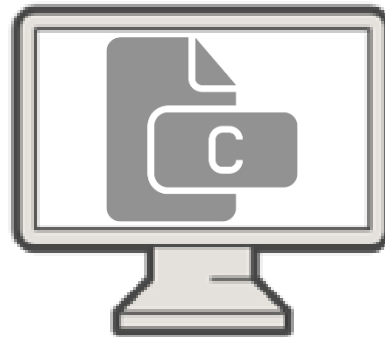
Verify the integration of Application SW with Basic SW

Validate the integration of one/few ECUs with simulated or real sensors, actuators, networks

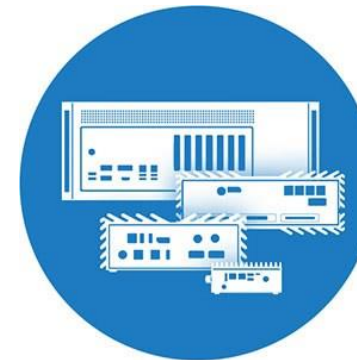
Validate the integration of **ALL** ECUs, Networks, Sensors and Actuators



MIL



SIL



HiL

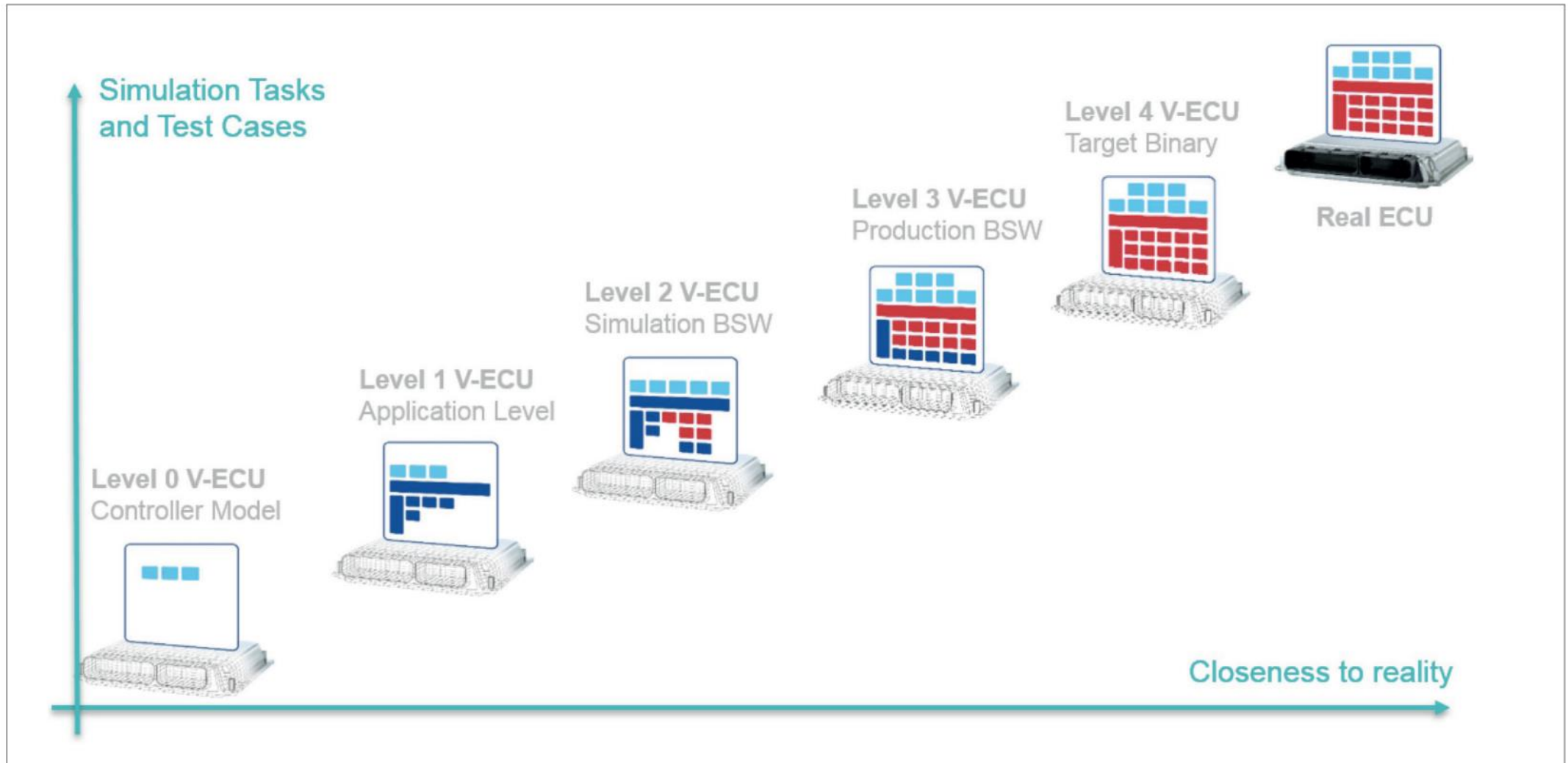


Vehicle

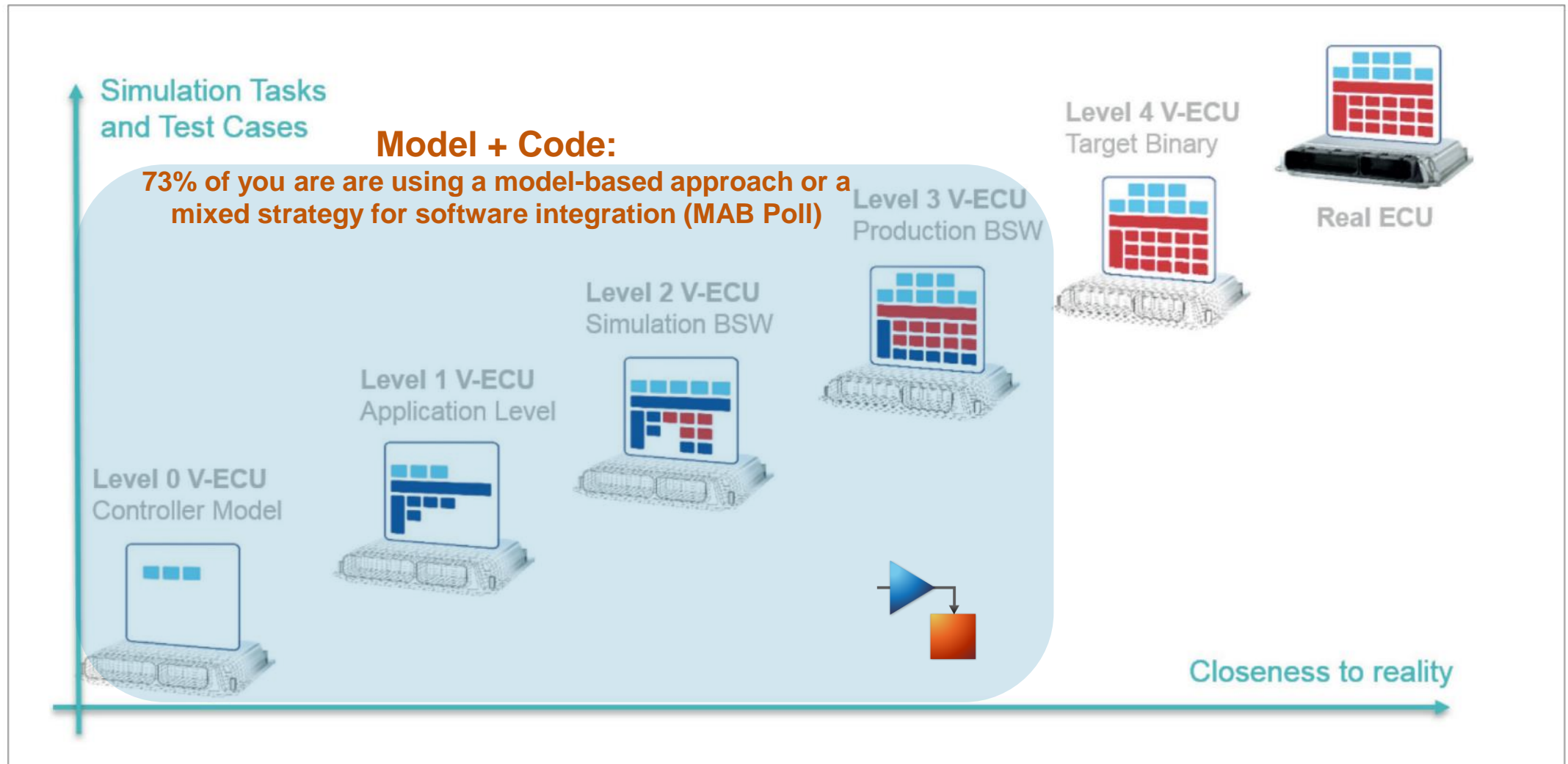


Key takeaway: use each test facility where it adds value during the process

V-ECU: Use cases vs level of fidelity



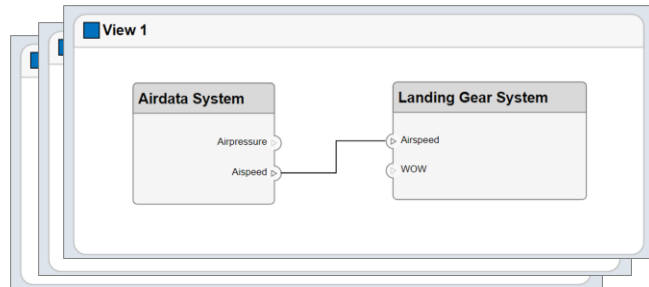
V-ECU with Simulink: Focus on where the complexity grows



Core capabilities we are focusing on



“Ready-to-run”
Model Components



Automated assembly
of models

```
class Component final
{
public:
// Real-time Model Data Structure
struct RT_MODEL_Component_T {
const char_T * volatile errorStatus;
};

// Copy Constructor
Component(Component const&) = delete;

// Assignment Operator
Component& operator= (Component const&) &= delete;

// Move Constructor
Component(Component &&) = delete;

// Move Assignment Operator
Component& operator= (Component &&) = delete;
```

“Ready-to-run”
Code Components



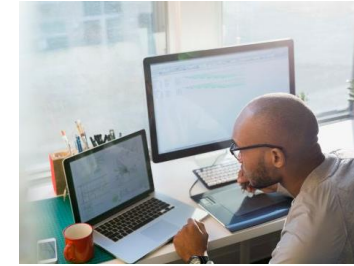
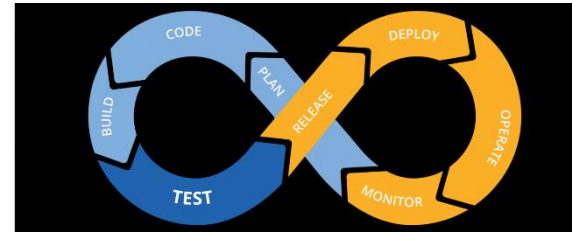
Performant Simulation!

Protected Models evolve to *Ready-to-run* models for integration

**FUTURE
RELEASE**

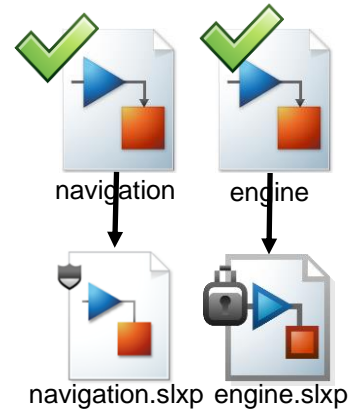


Creator

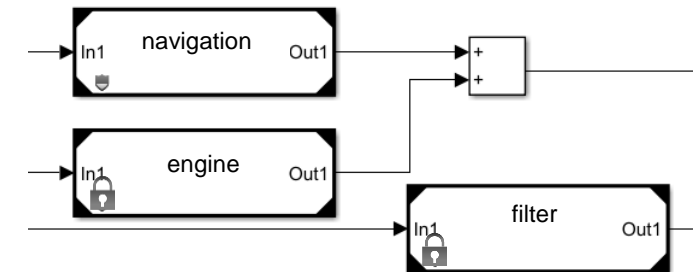
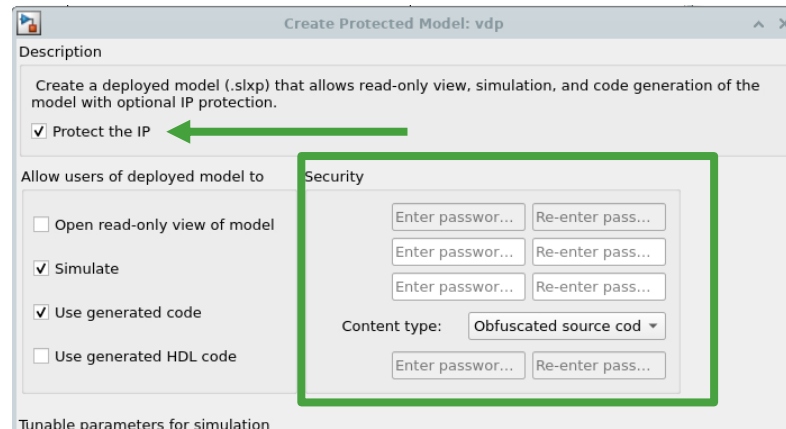


Recipient

Inside/outside the org



Package **verified** component for ready-to-run

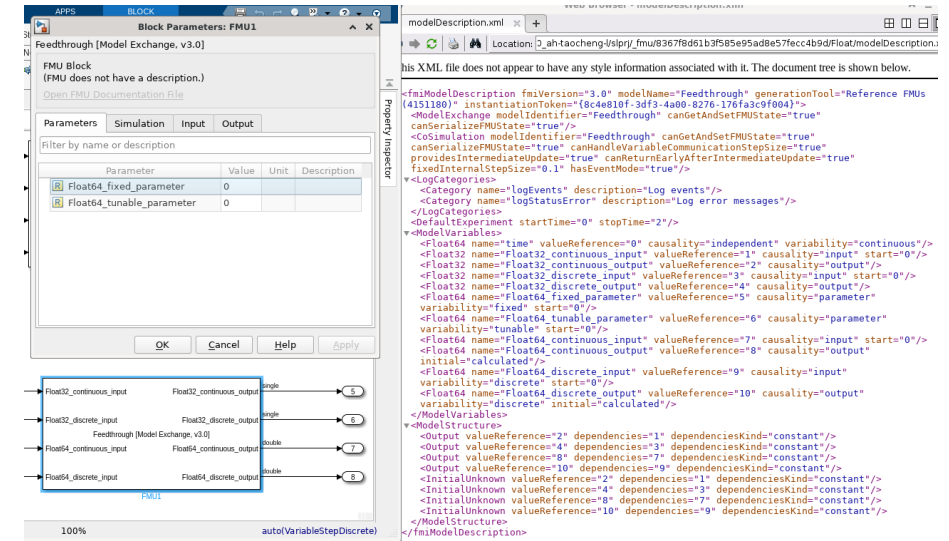


Benefits

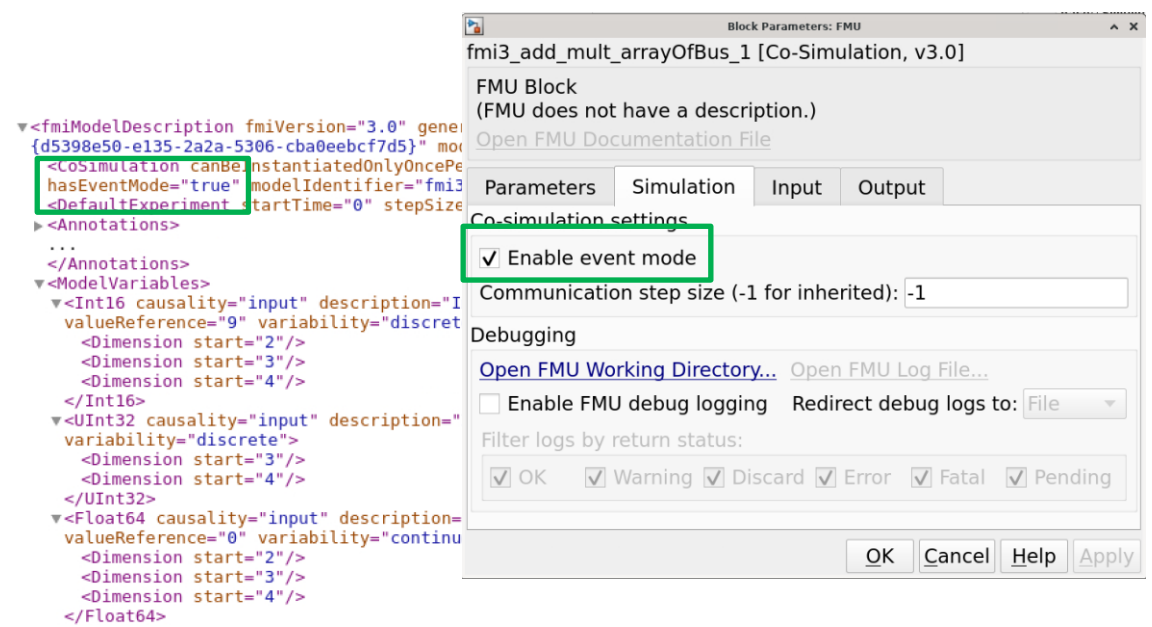
- Flexibility
- Performance
- Encapsulation
- Multi-instantiation
- ...

FMUs continue to provide an avenue to make ready-to-run Parts from other tools

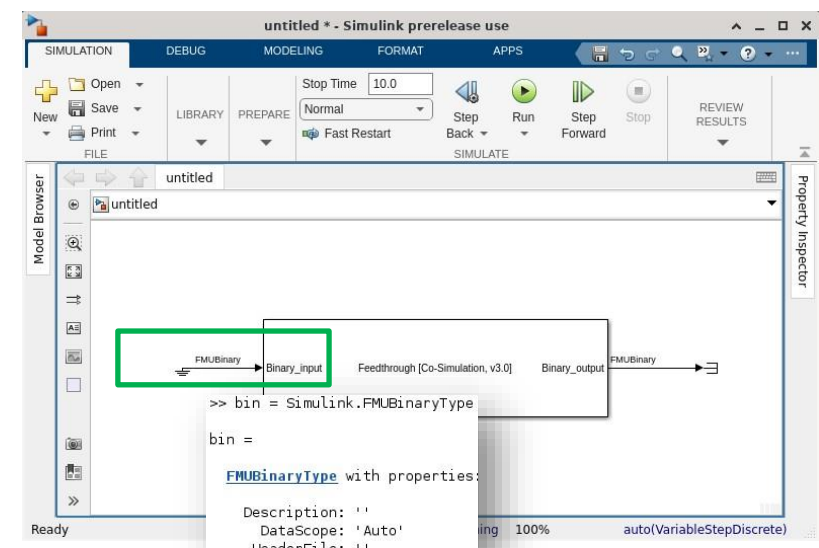
- Simulink supports FMI 3.0 Import in R2023b



FMU Import block loading FMU 3.0 modelDescription file

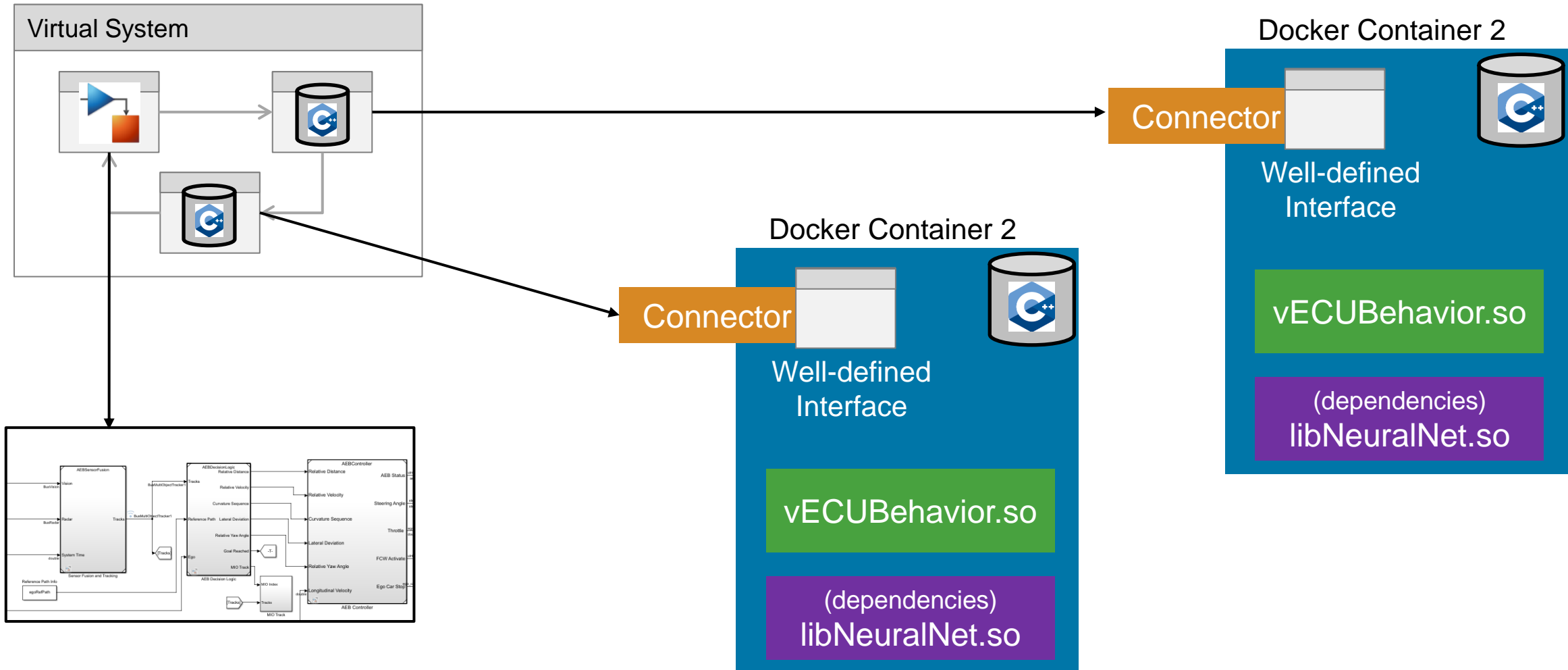


FMU Co-simulation with event mode eliminates one-step delay



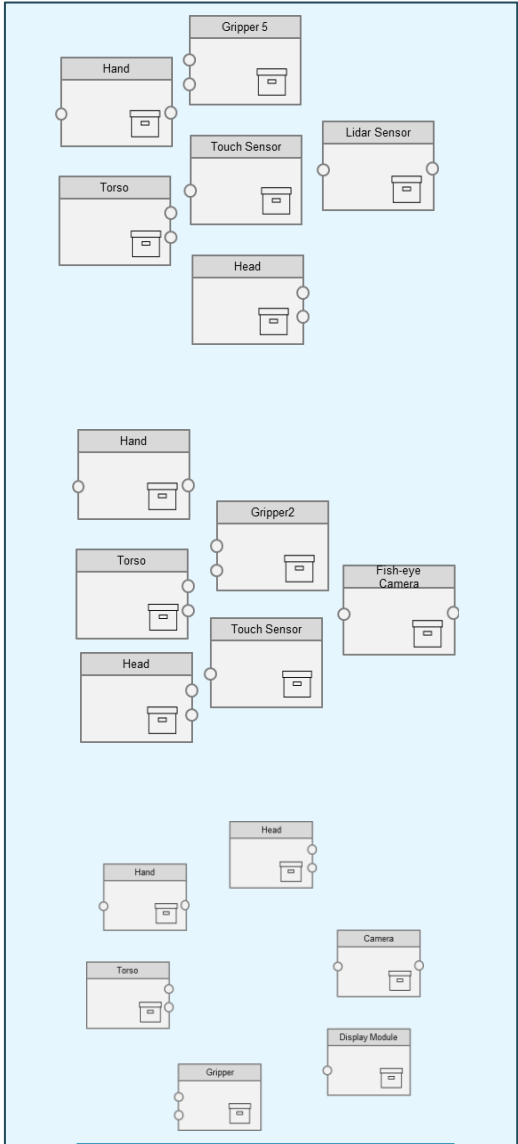
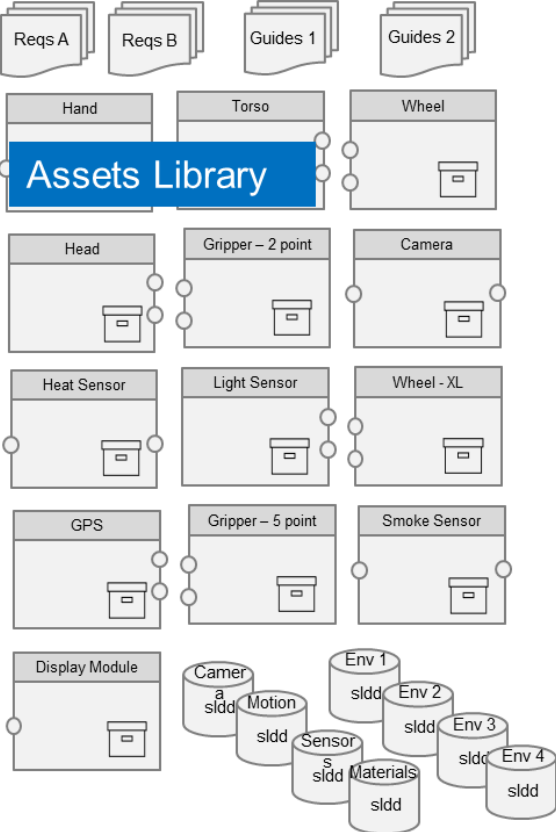
FMUBinary data type

Connector for ready-to-run code components

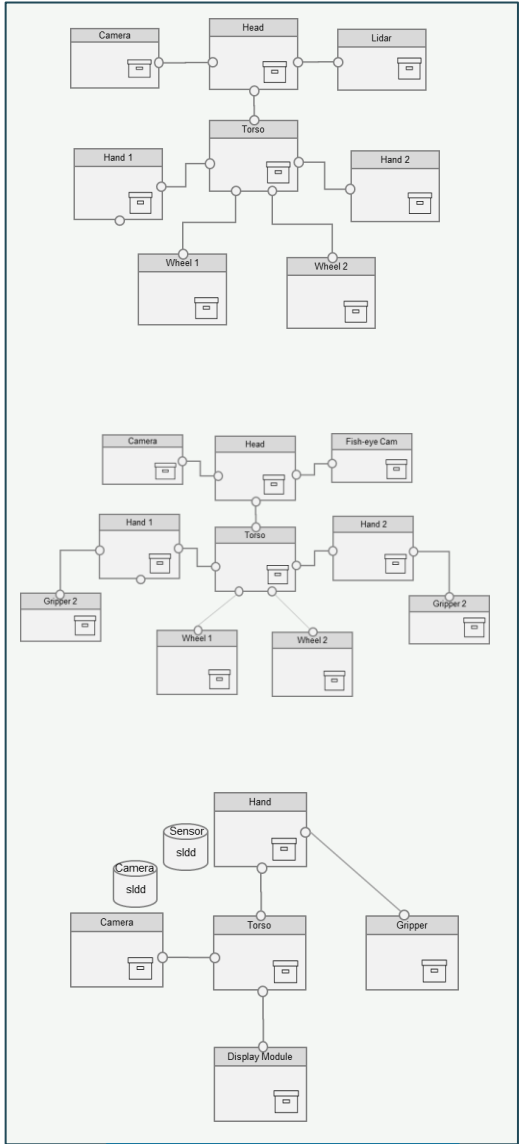


Unit testable, distributable containers

Auto model assembly is being emphasized in many workflows



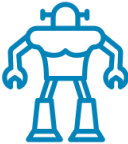
Selection of Parts



Model Assembly



Explorer

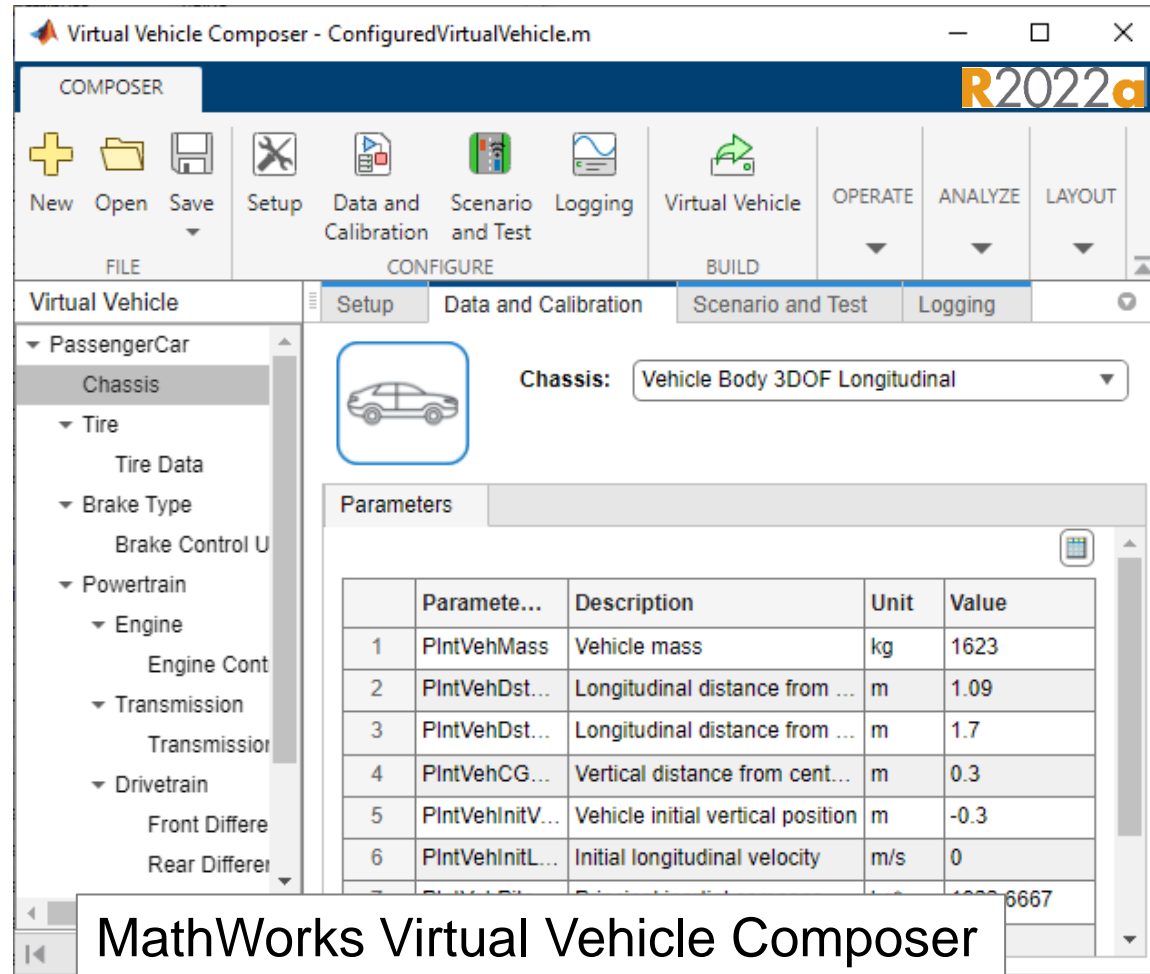


Rescuer



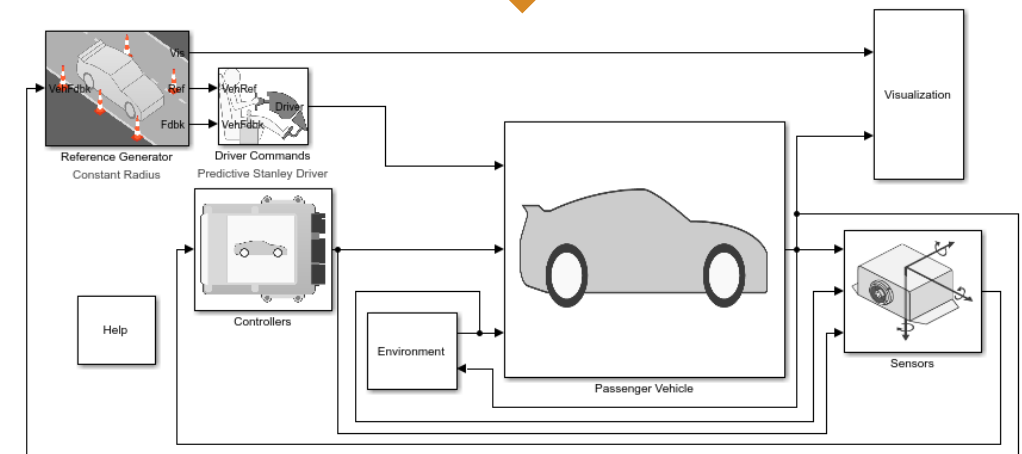
Picker

We are creating a “Feature-Driven” Approach to picking the Parts to Assemble into Models



MathWorks Virtual Vehicle Composer
Select Vehicle features & characteristics

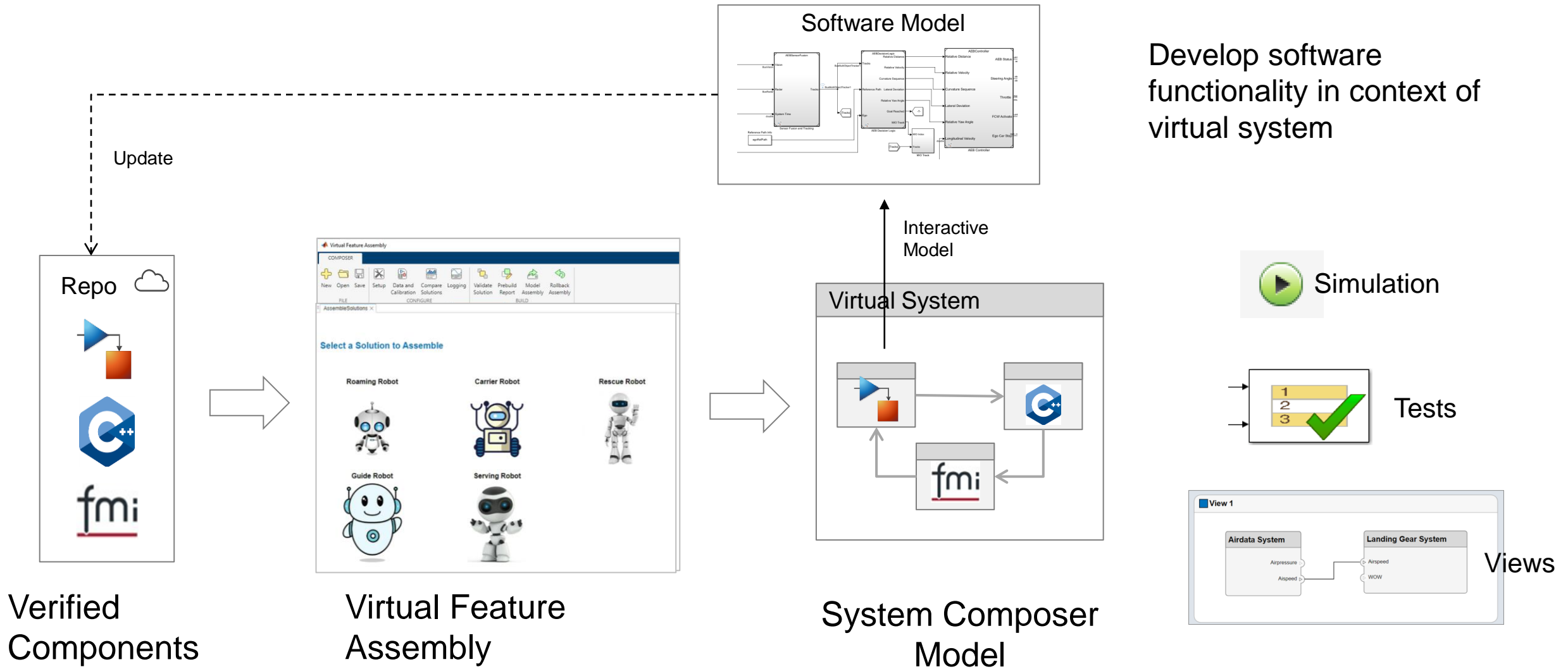
Your Component
Under Test

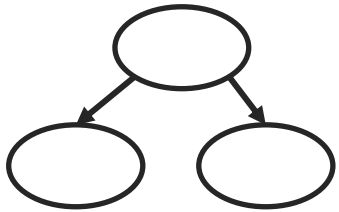


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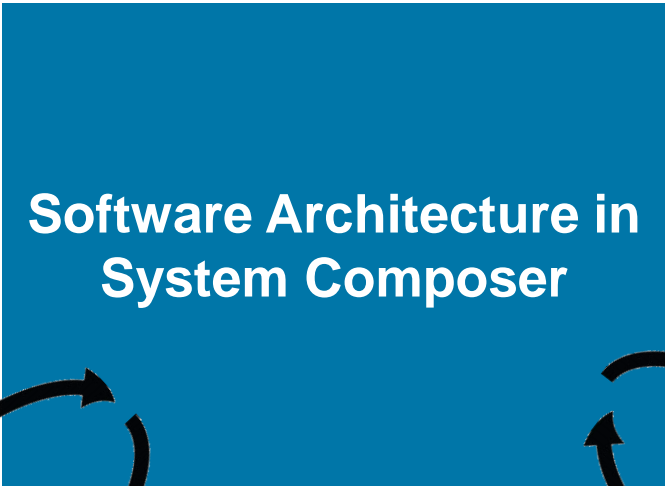
Assembled Vehicle Model

Towards a fully distributed “model and code” integration framework

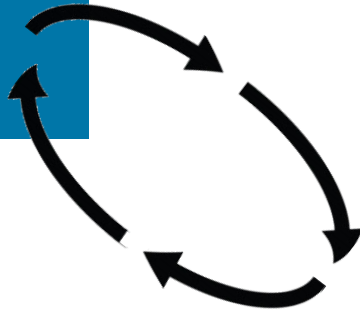
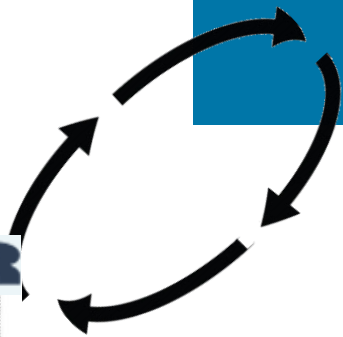
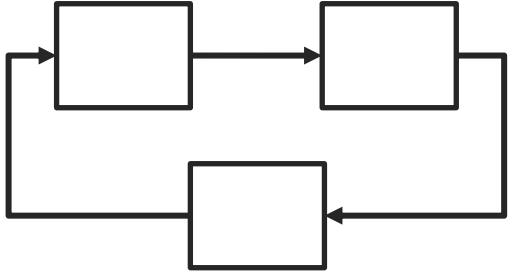




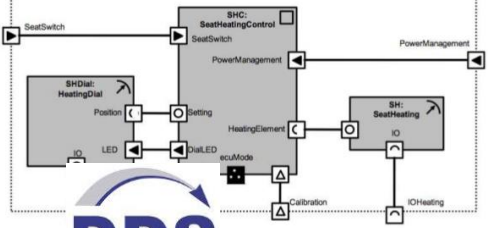
Requirements



Design Models



AUTOSAR



DDS

Software Frameworks & SOA

Virtual integration



How are we taking this journey?

SDV: Integrating Simulink C++ Code in Android Automotive Environment

Rémy Brugnon, Renault Group



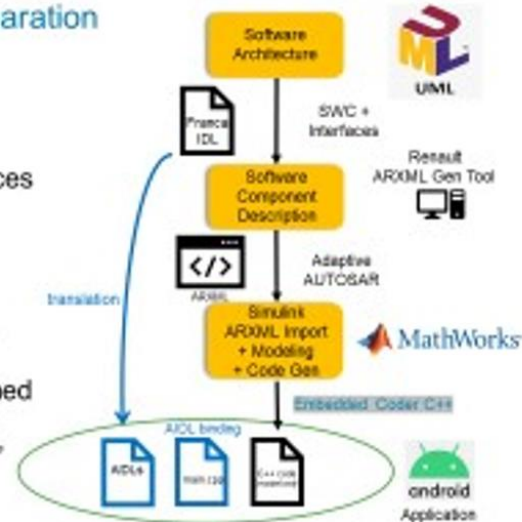

Use Model-Based Design to Develop SOA Application Running on In-vehicle OS

Weiwei Luo, ZEEKR TECHNOLOGY LIMITED



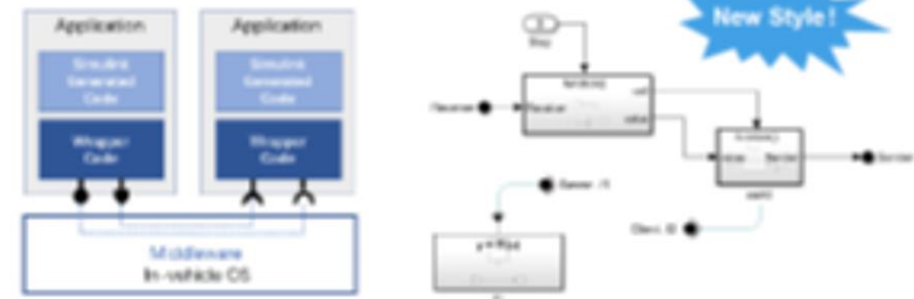

Context: Renault SDV Project preparation

- Renault strategic collaboration with Google: Android Automotive OS replaces Adaptive AUTOSAR
- New Interface Definition Language: Android IDL (used for IPC generation)
- Service Oriented Architecture maintained (Request/Response methods => RPC, events => RPC + Callbacks)



RPC: Remote Procedure Call
IPC: Inter-Process Communication

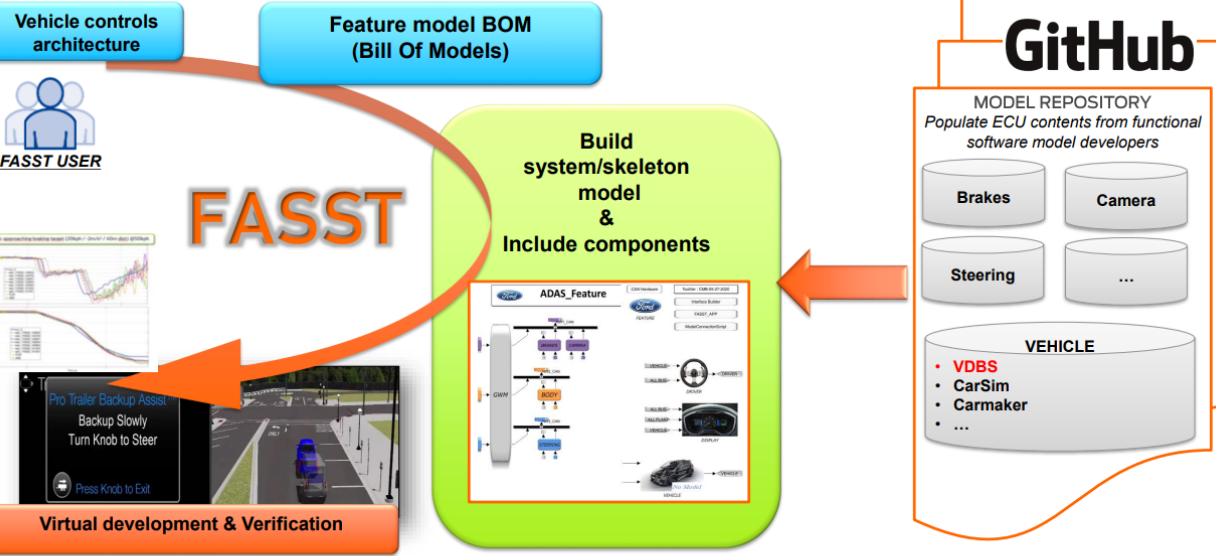
How to Model on In-vehicle OS



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Industry practice examples

FORD AUTOMATED SYSTEM SIMULATION TOOLCHAIN (FASST)

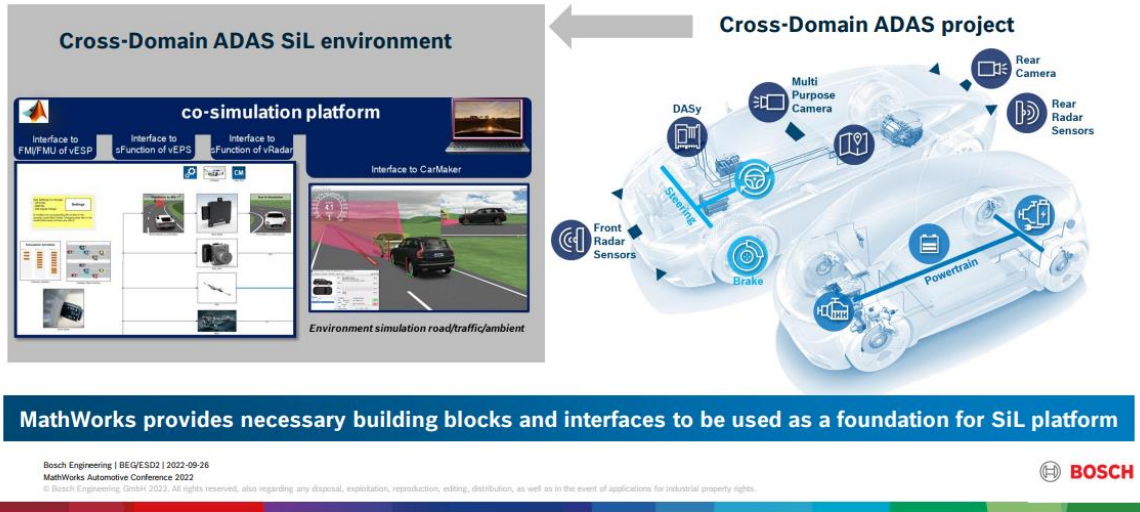


FASST reduced virtual vehicle build from months into minutes



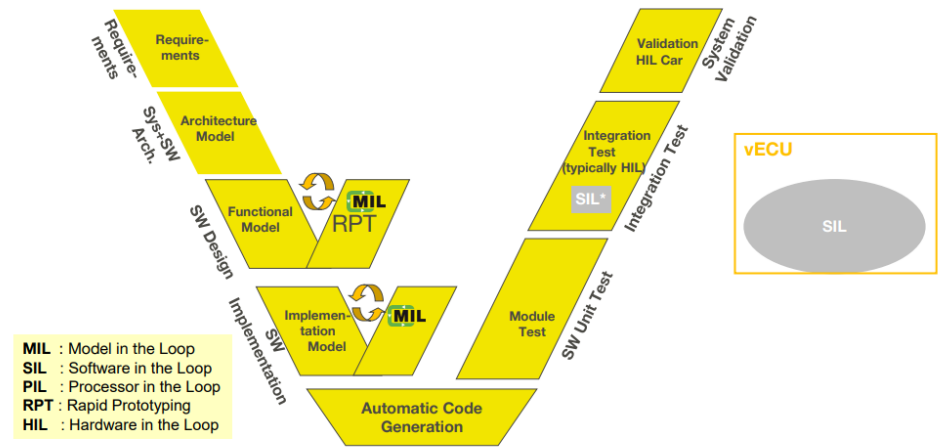
MATLAB/Simulink-based Cross-Domain SiL platform

Overview and context of SiL platform



VALIDATION OF AUTOSAR SOFTWARE VIA VECU

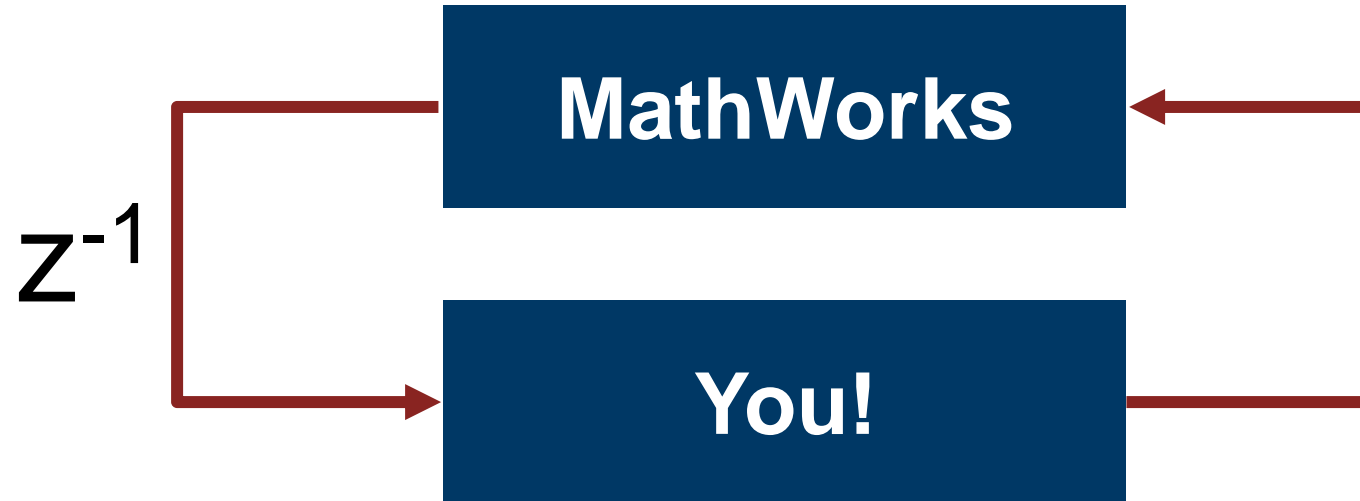
GOAL OF VECU - ALIGNMENT INTO V-CYCLE DEVELOPMENT PROCESS



- MIL** : Model in the Loop
- SIL** : Software in the Loop
- PIL** : Processor in the Loop
- RPT** : Rapid Prototyping
- HIL** : Hardware in the Loop

Simulation of complete ECU Software (Production ASW-Code)
Can be used for integration tests before going to the HIL





MathWorks



You!

Questions